

DIAGRAMA DE CIRCUITO

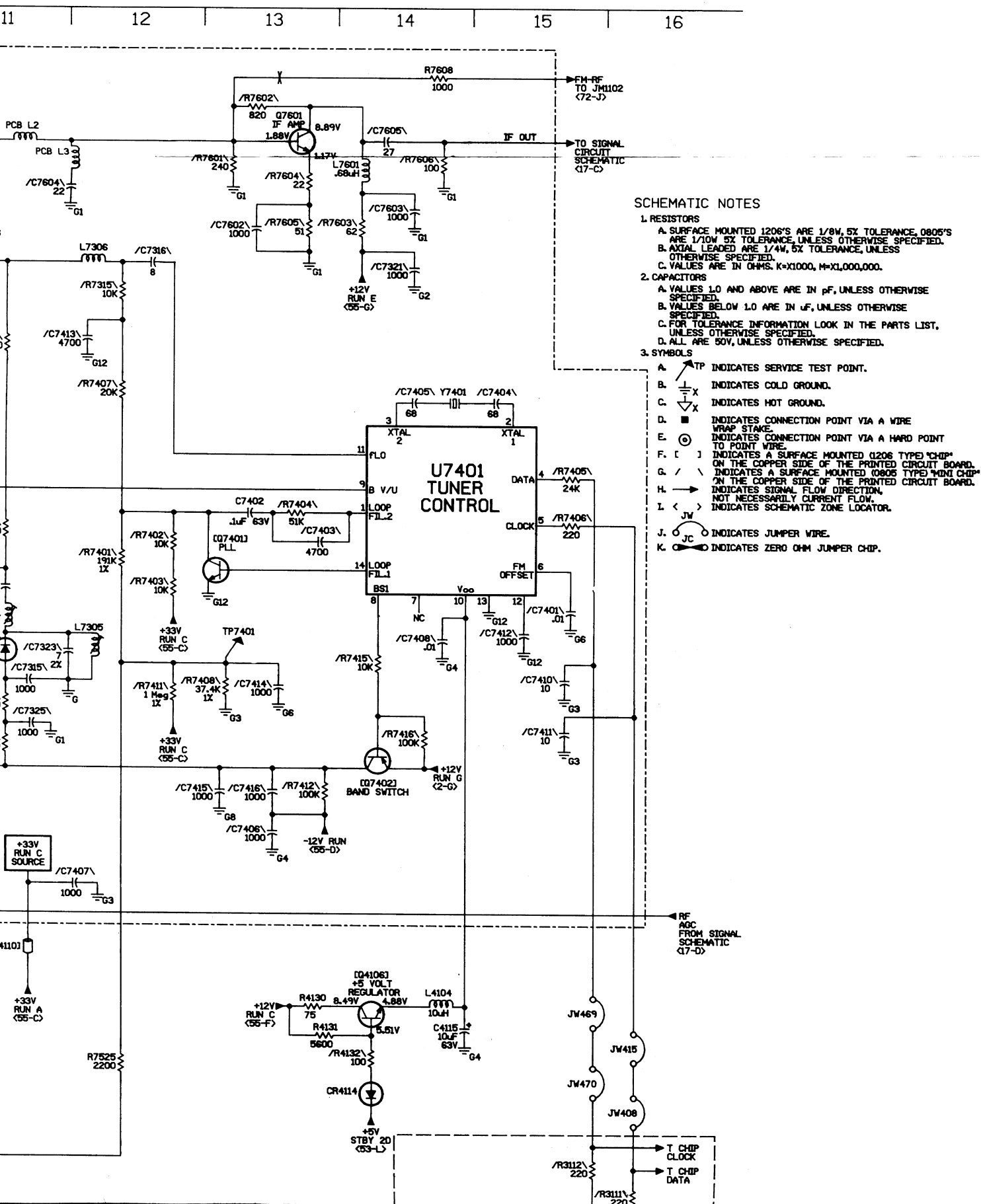
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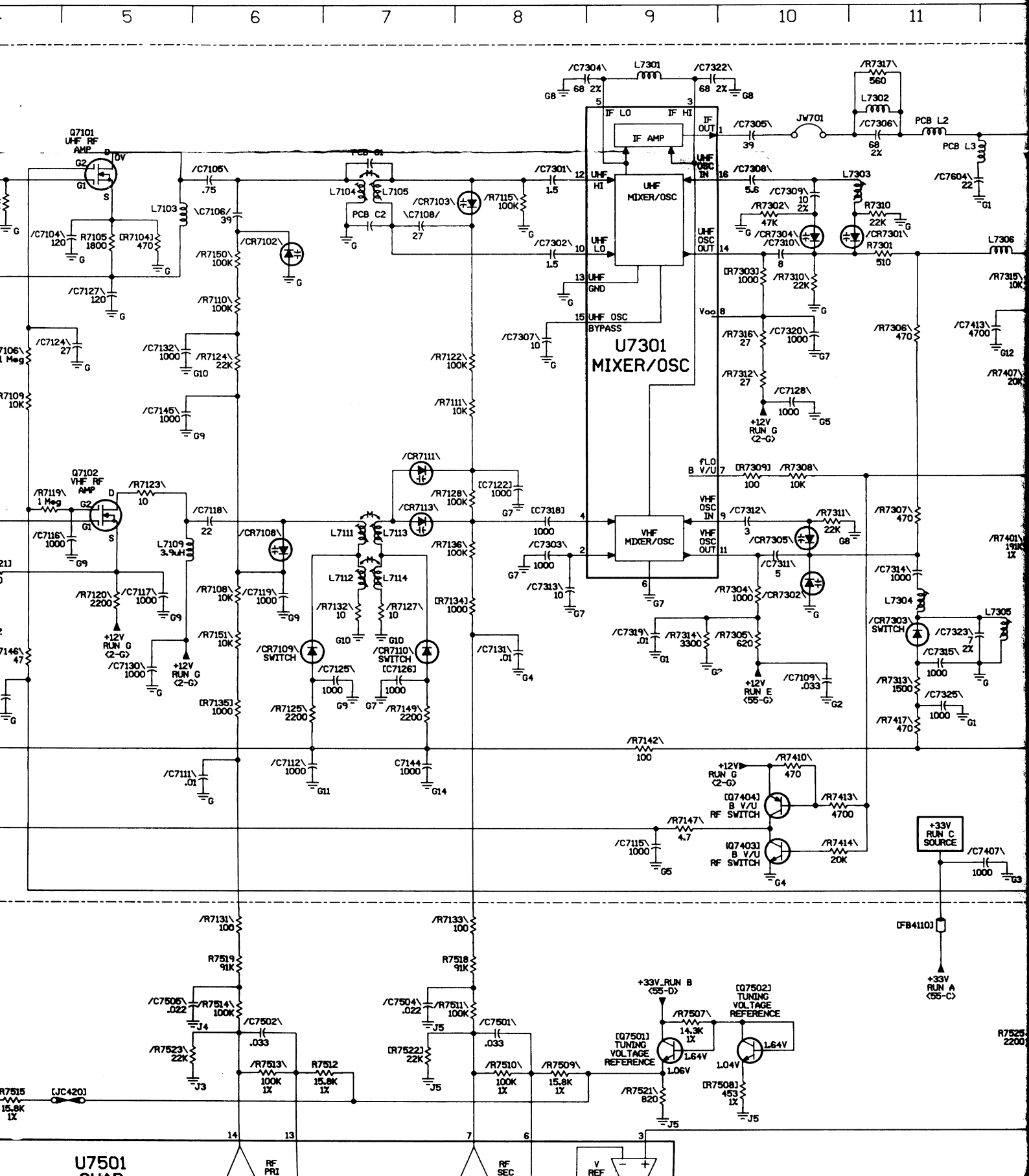


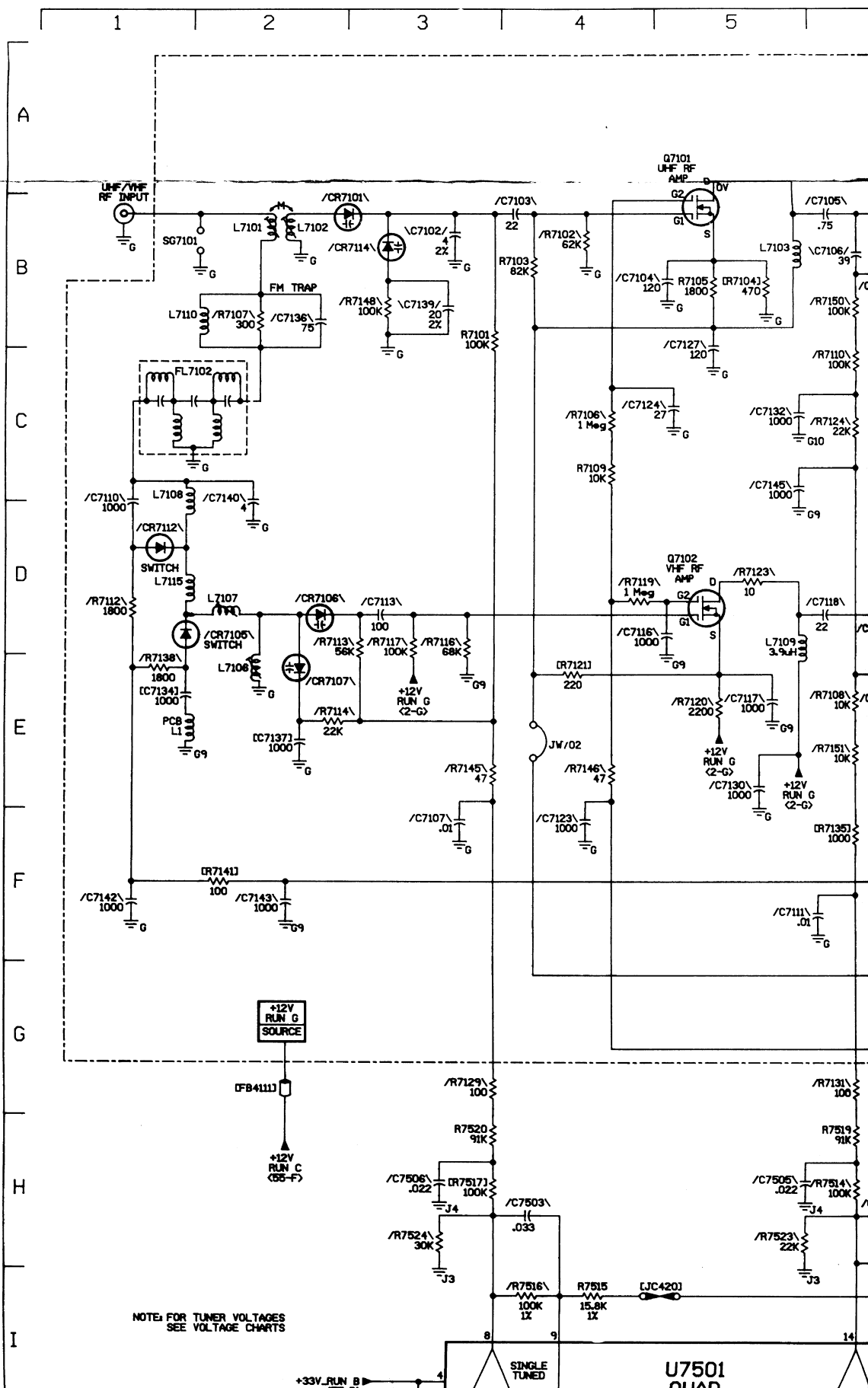
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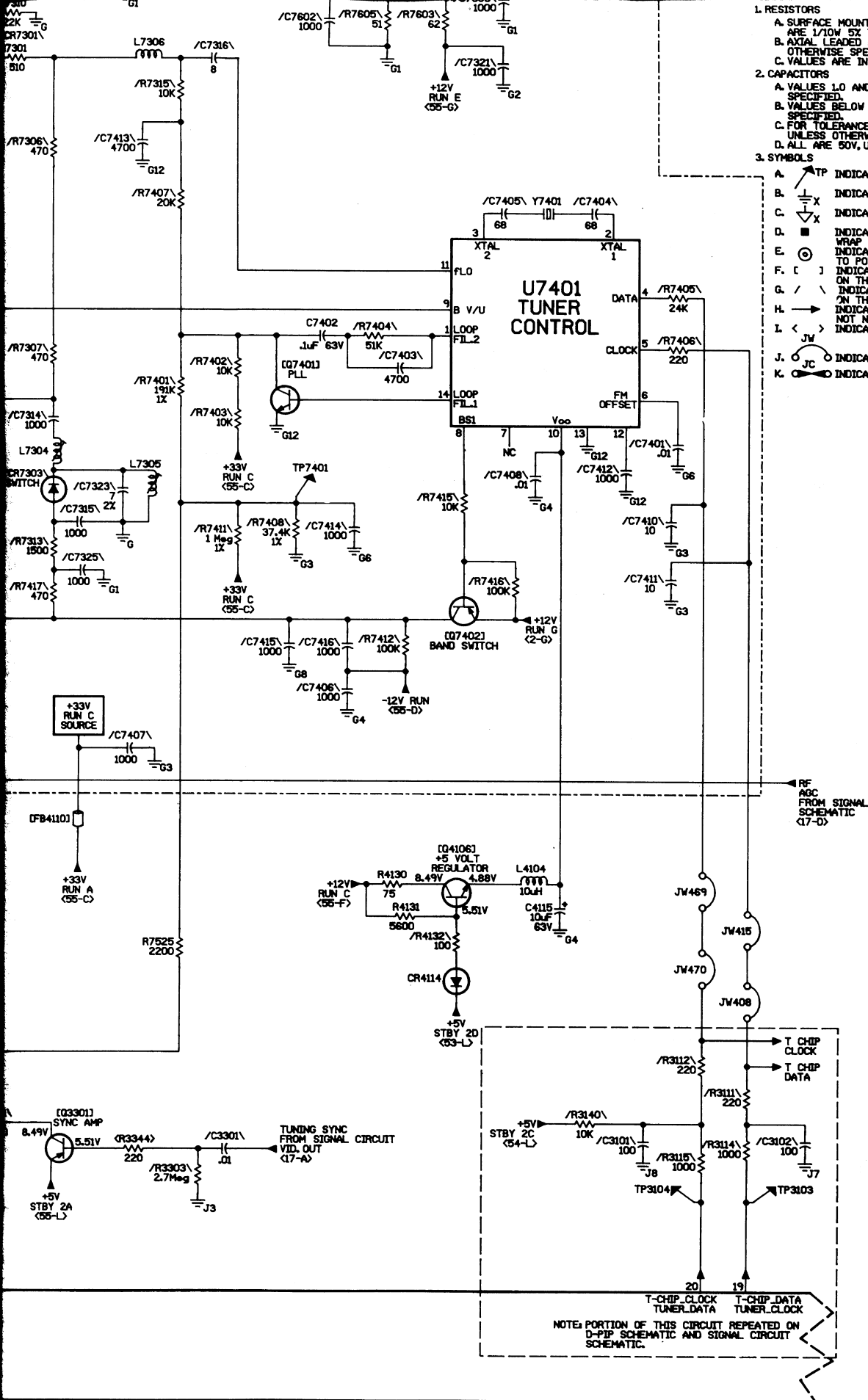
RCA/GE

DIAGRAMA ESQUEMÁTICO DO TUNER











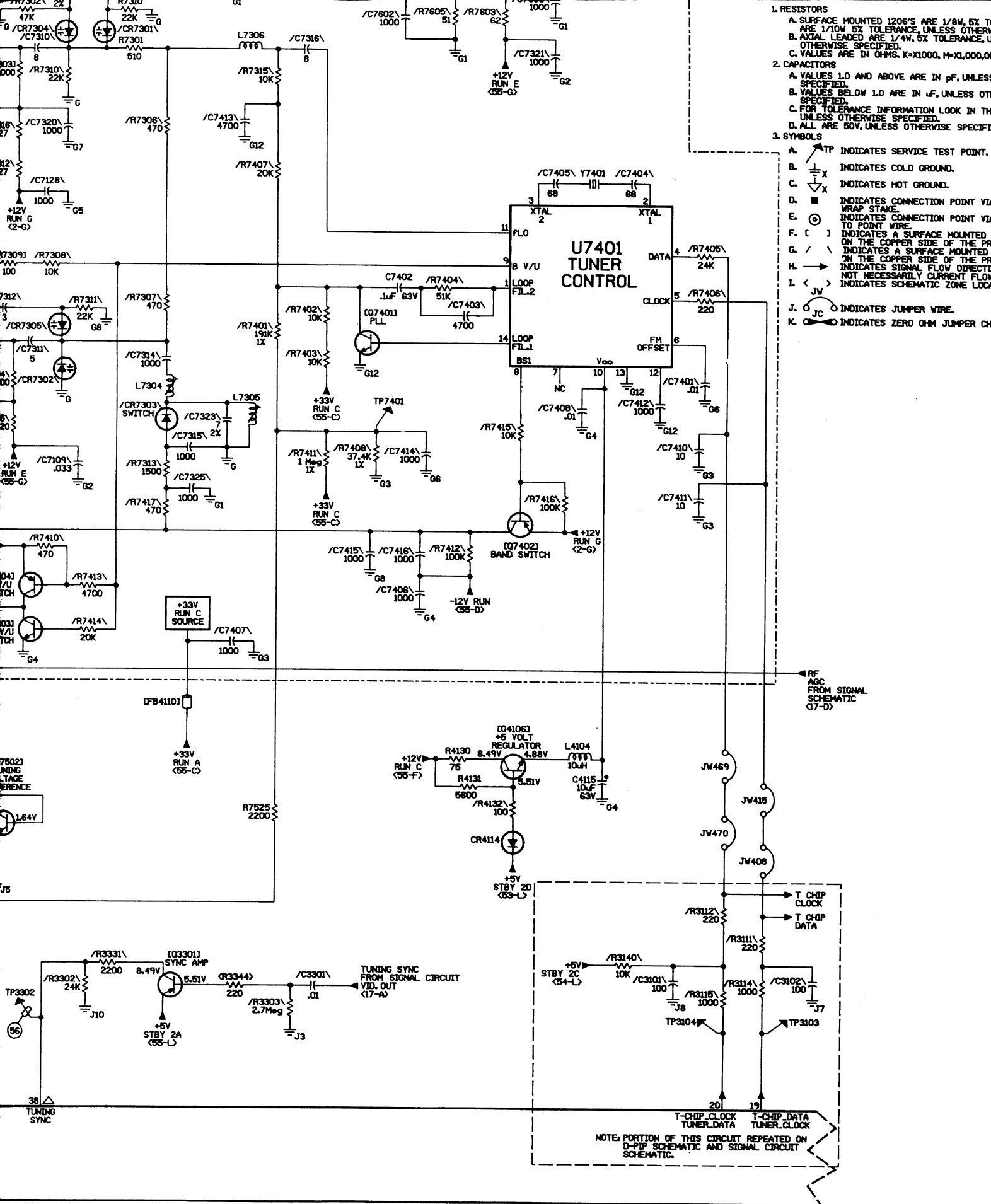
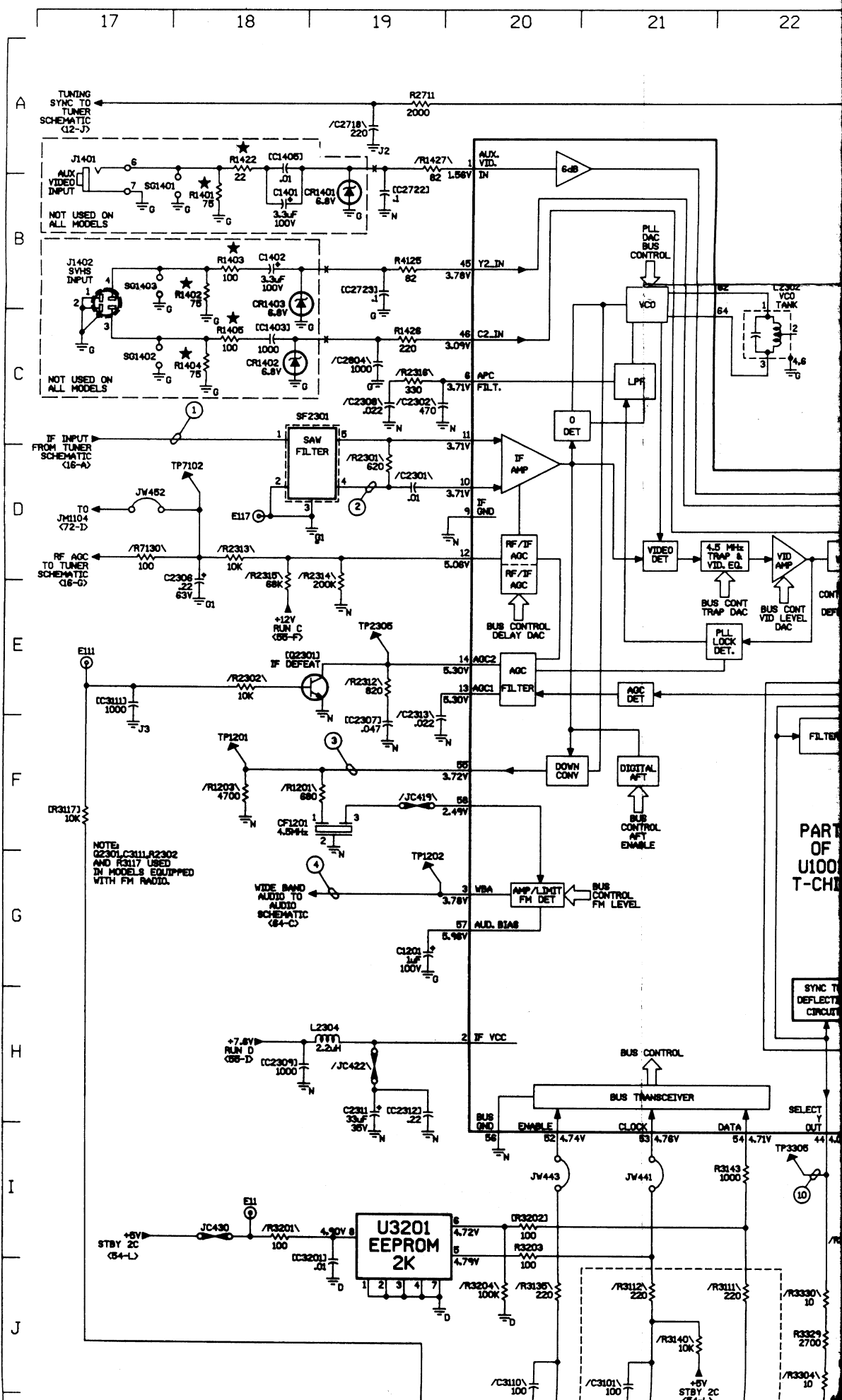
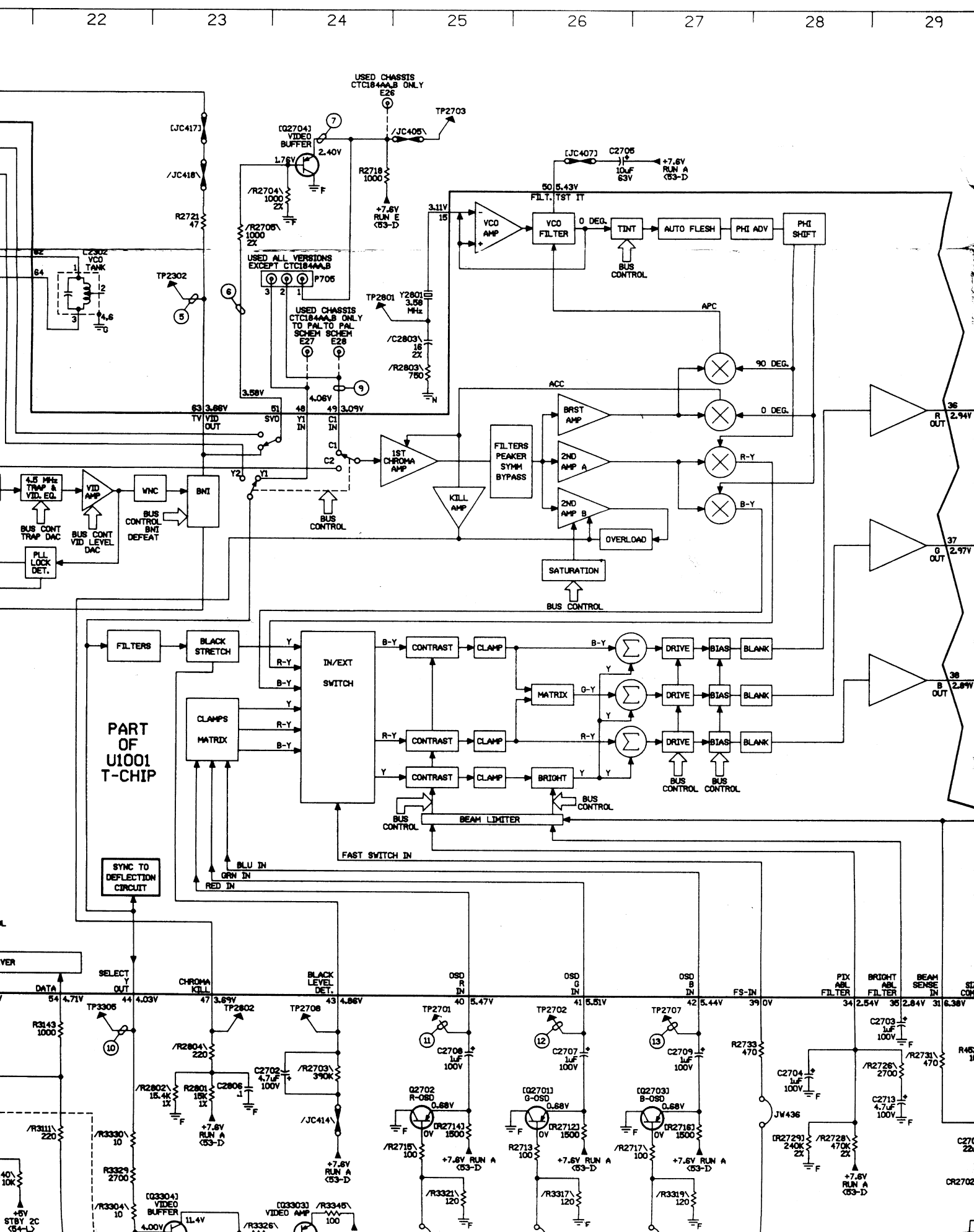


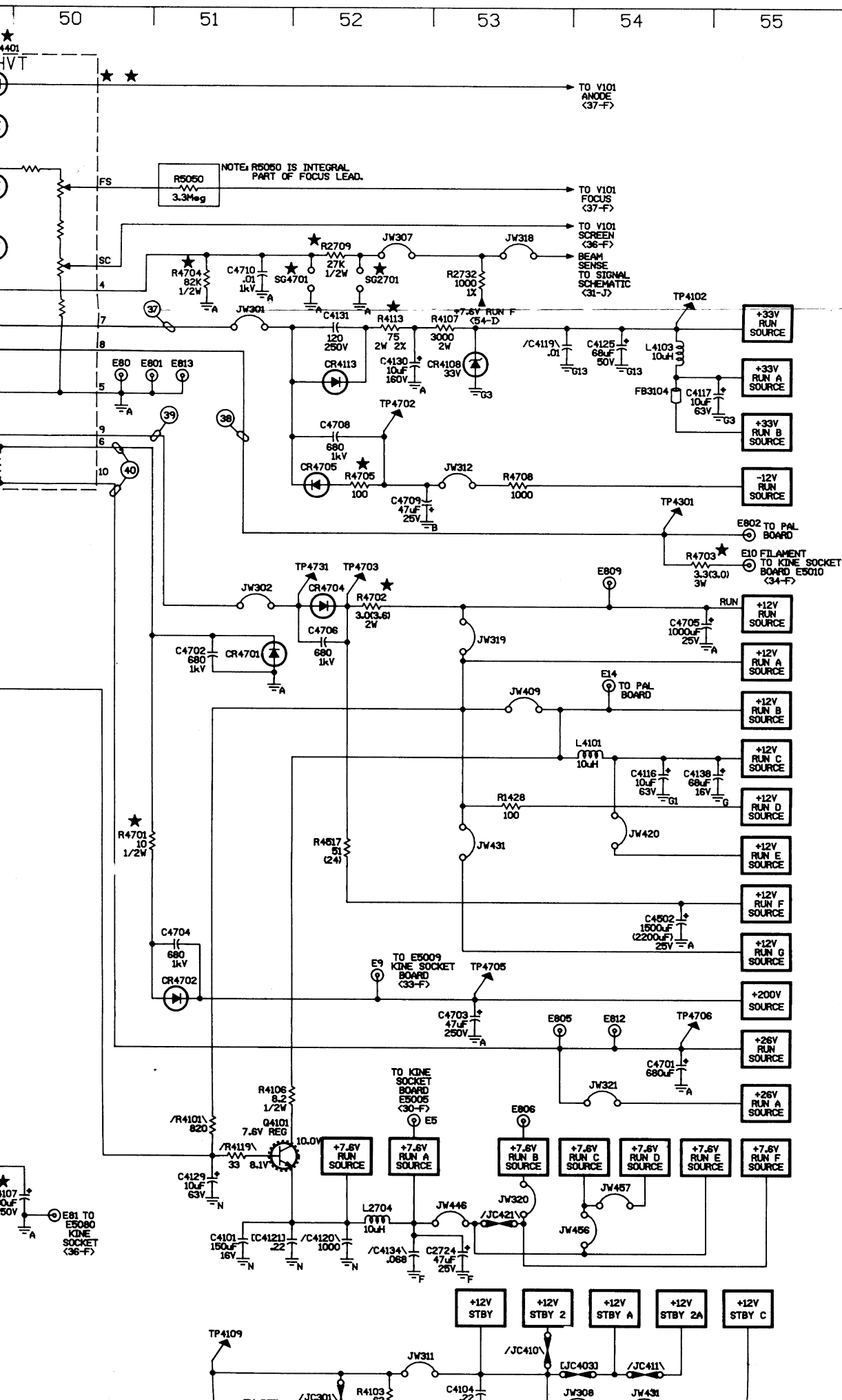
DIAGRAMA ESQUEMÁTICO DO SINAL

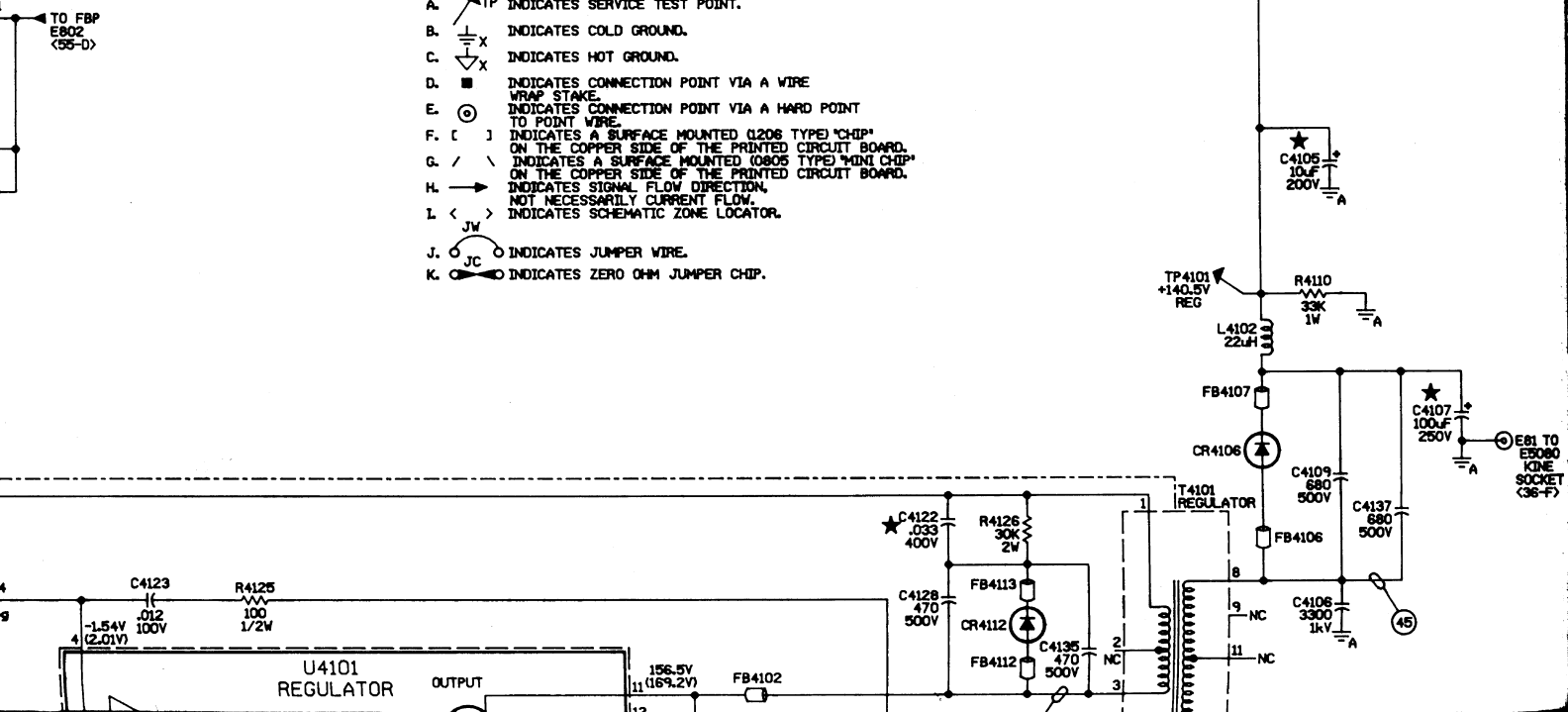






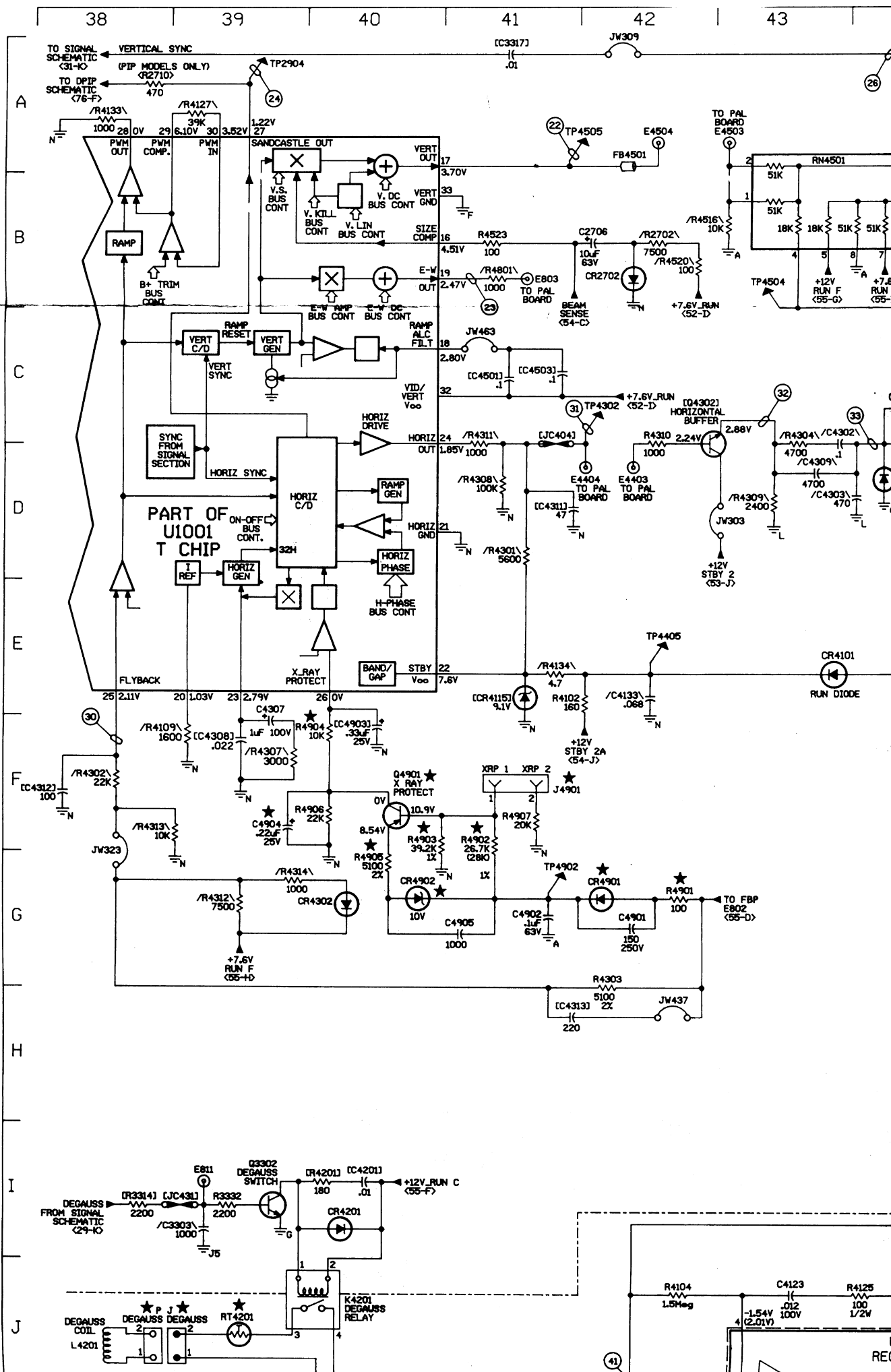
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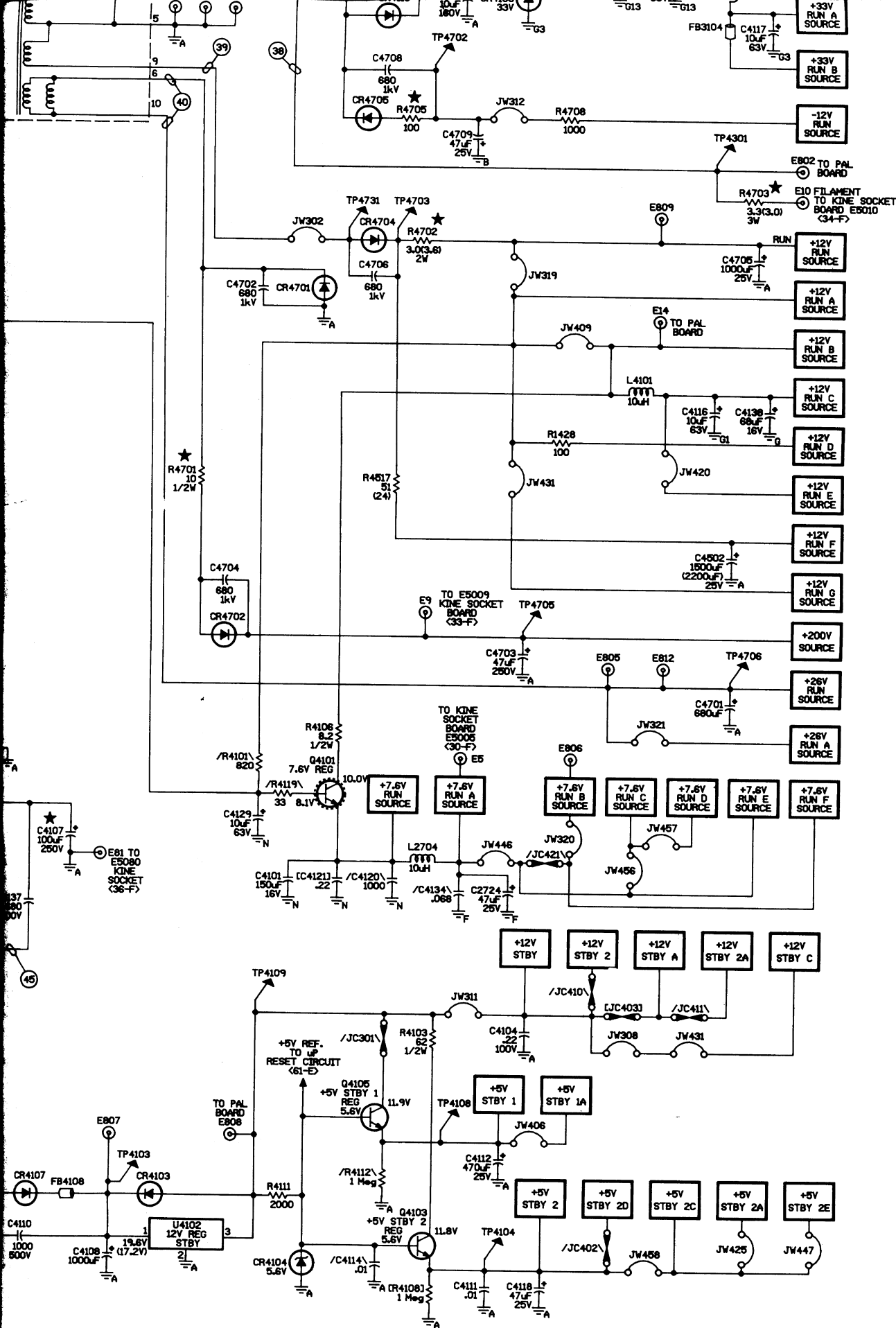




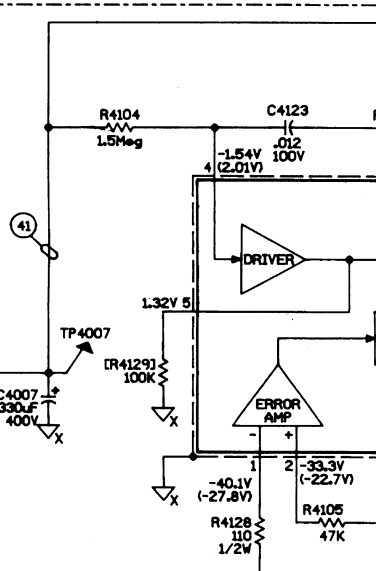
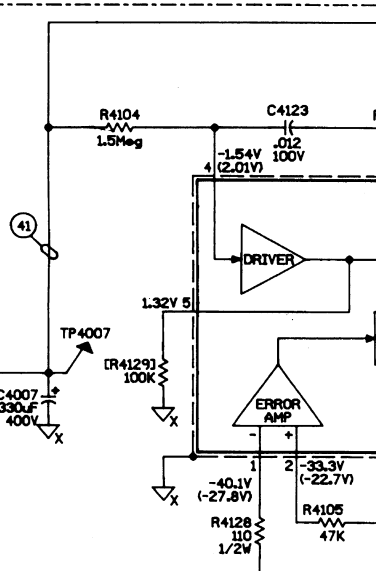
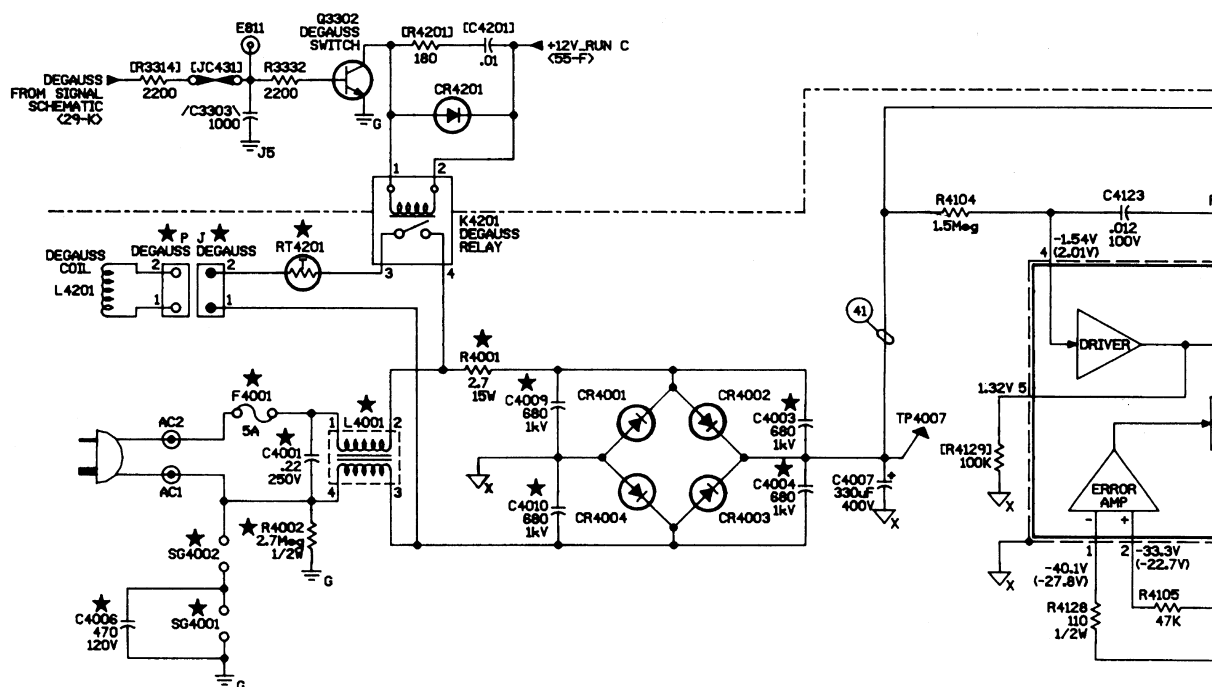
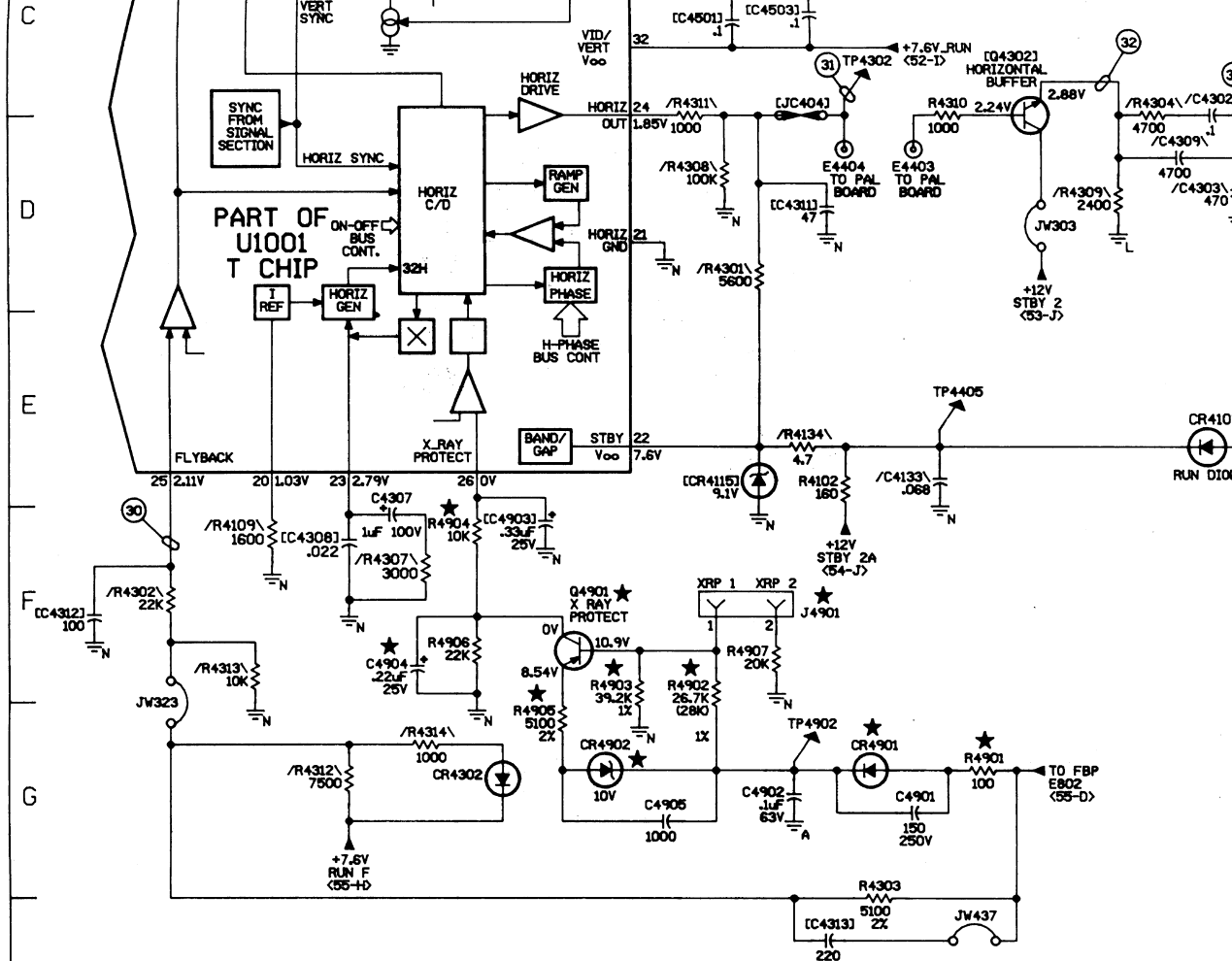
1. RESISTORS

- ### RESISTORS
- A. SURFACE MOUNTED 1206'S ARE 1/8W, 5% TOLERANCE, 0805'S ARE 1/10W, 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
B. AXIAL LEADED ARE 1/4W, 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
C. VALUES ARE IN OHMS, K=X1000, M=X1,000,000.
- ### 2. CAPACITORS
- A. VALUES 1.0 AND ABOVE ARE IN pF, UNLESS OTHERWISE SPECIFIED.
B. VALUES BELOW 1.0 ARE IN uF, UNLESS OTHERWISE SPECIFIED.
C. FOR TOLERANCE INFORMATION LOOK IN THE PARTS LIST, UNLESS OTHERWISE SPECIFIED.
D. ALL ARE 50V, UNLESS OTHERWISE SPECIFIED.
- ### 3. SYMBOLS
- A.  TP INDICATES SERVICE TEST POINT.
B.  X INDICATES COLD GROUND.
C.  X INDICATES HOT GROUND.
D.  ■ INDICATES CONNECTION POINT VIA A WIRE WRAP STAKE.
E.  ○ INDICATES CONNECTION POINT VIA A HARD POINT TO POINT WIRE.
F. [] INDICATES A SURFACE MOUNTED (1206 TYPE) "CHIP" ON THE COPPER SIDE OF THE PRINTED CIRCUIT BOARD.
G. / \ INDICATES A SURFACE MOUNTED (0805 TYPE) "MINI CHIP" ON THE COPPER SIDE OF THE PRINTED CIRCUIT BOARD.
H.  → INDICATES SIGNAL FLOW DIRECTION, NOT NECESSARILY CURRENT FLOW.
I. < > INDICATES SCHEMATIC ZONE LOCATOR.
J.  JW INDICATES JUMPER WIRE.
K.  JC INDICATES ZERO OHM JUMPER CHIP.











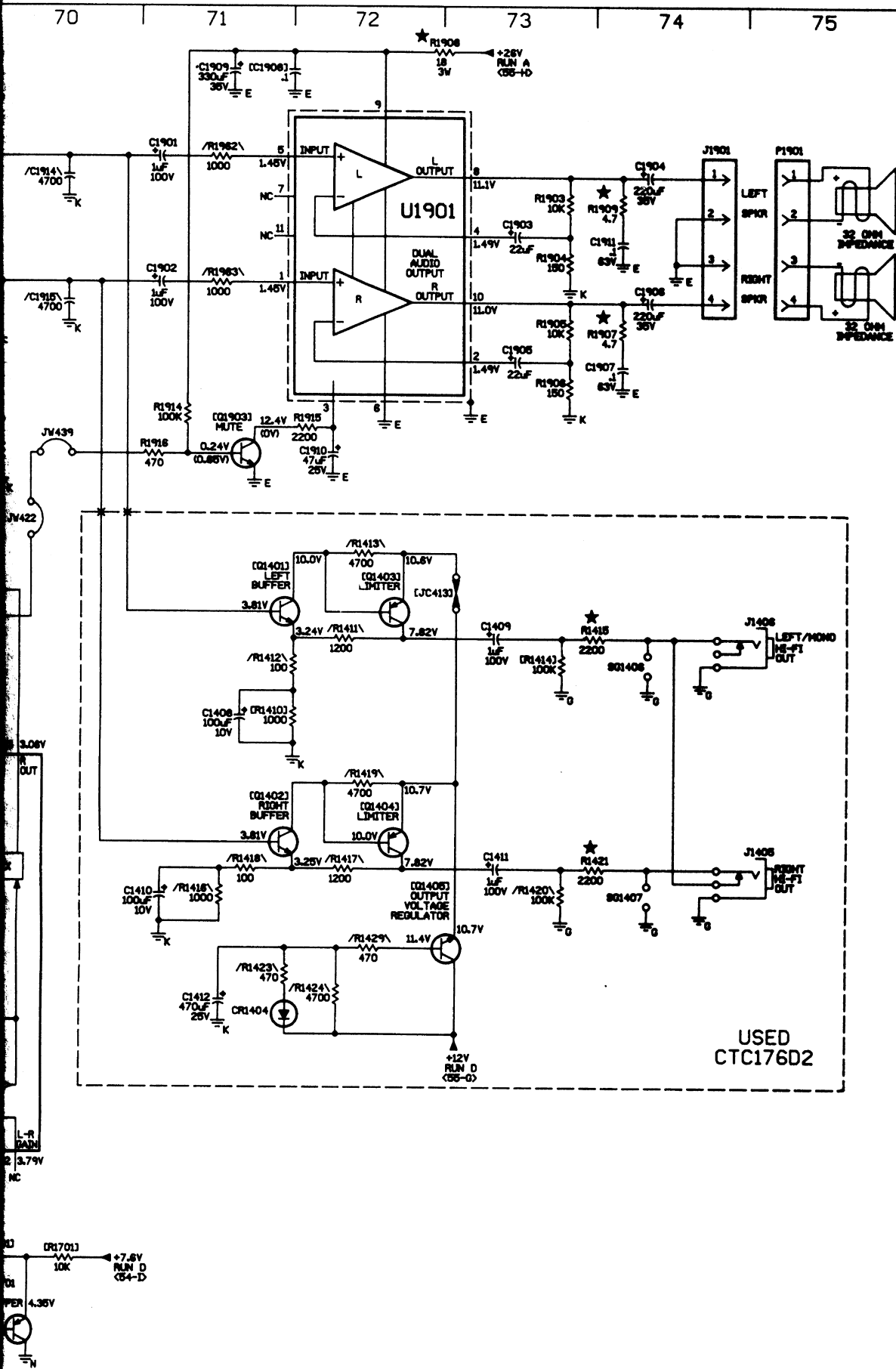
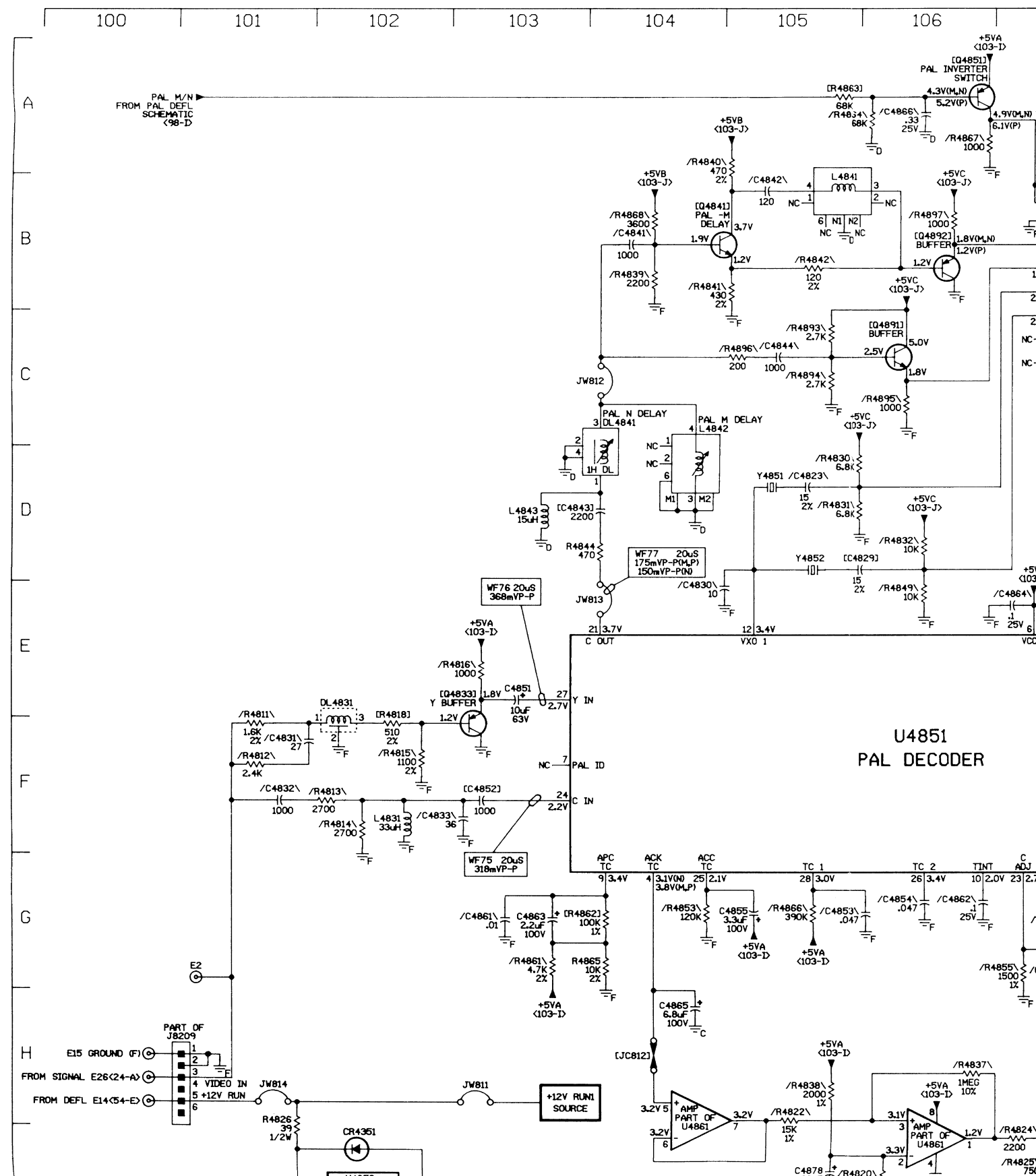
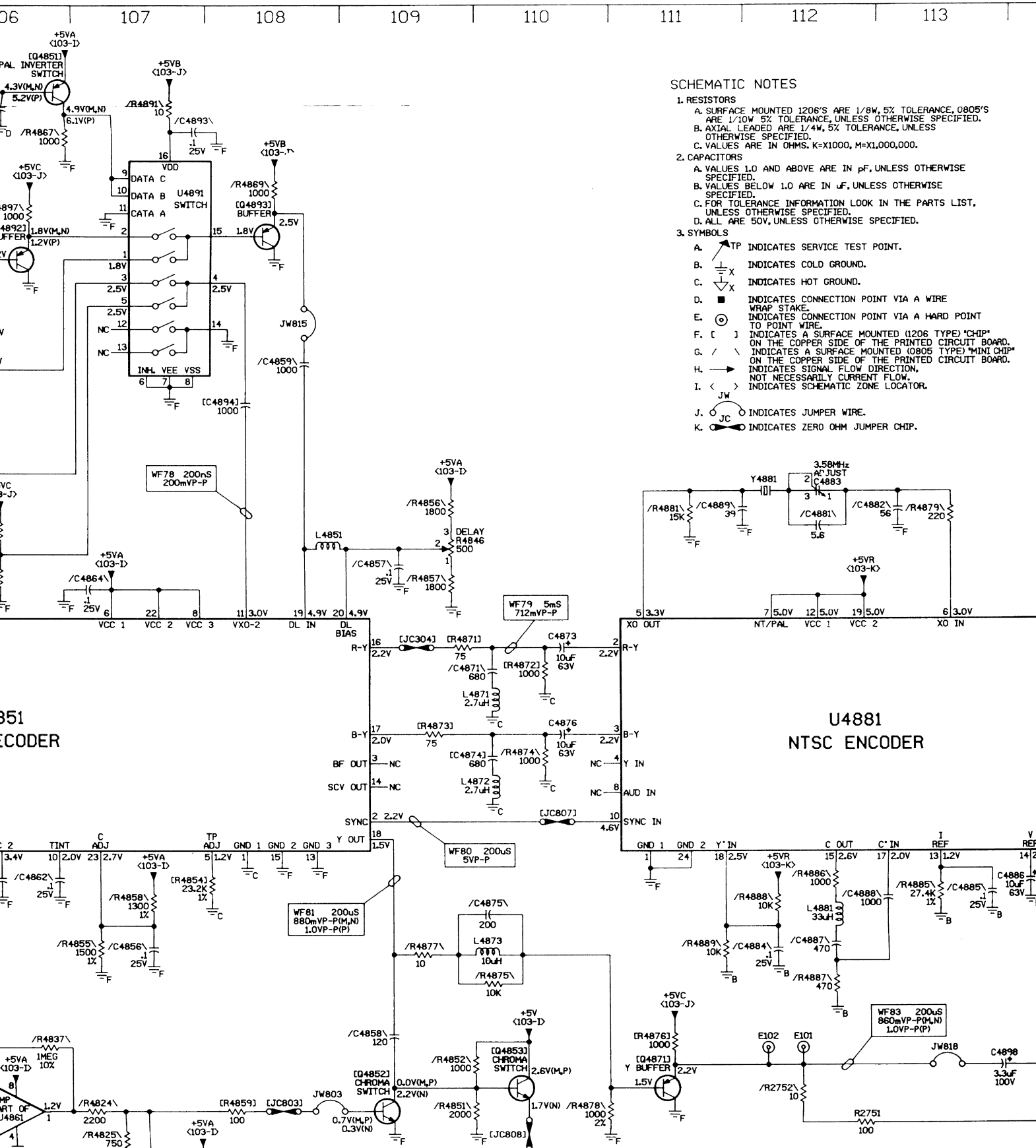


DIAGRAMA ESQUEMÁTICO DO SINAL DE PAL





113 | 114 | 115 | 116 | 117

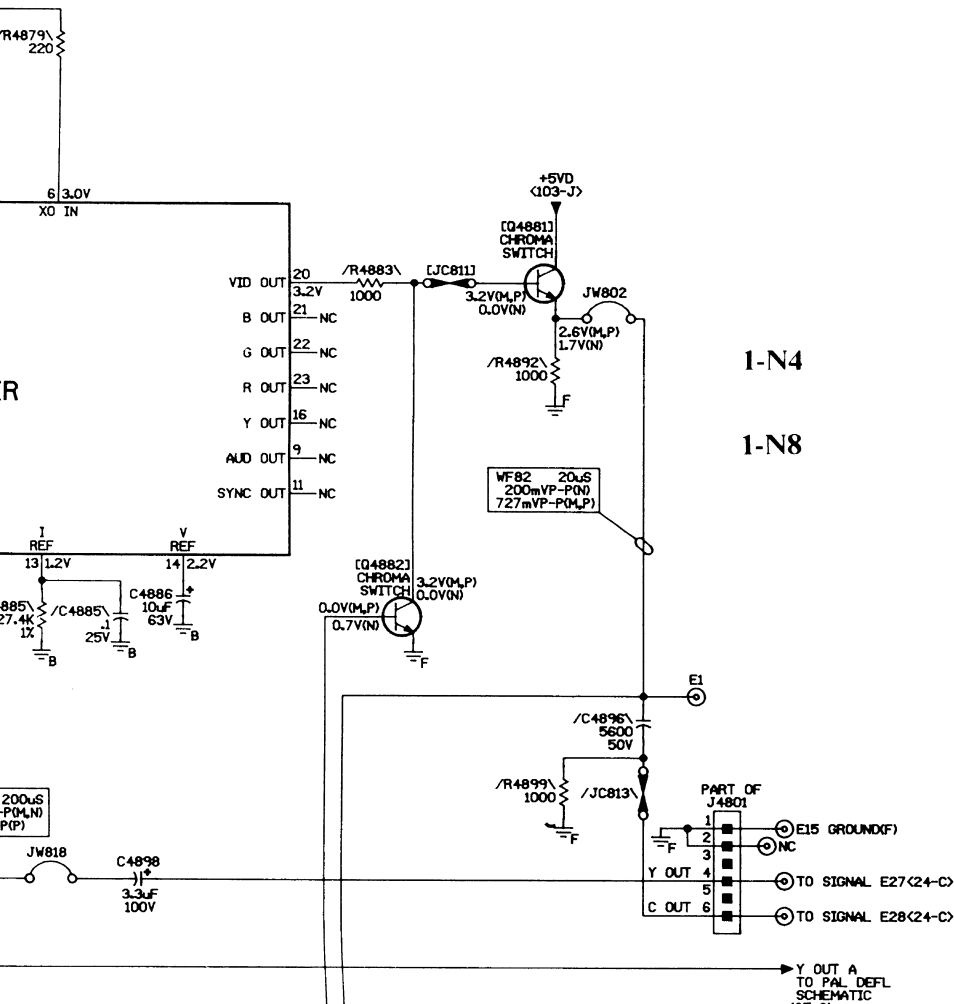
TOLERANCE, 0805'S
UNLESS SPECIFIED.

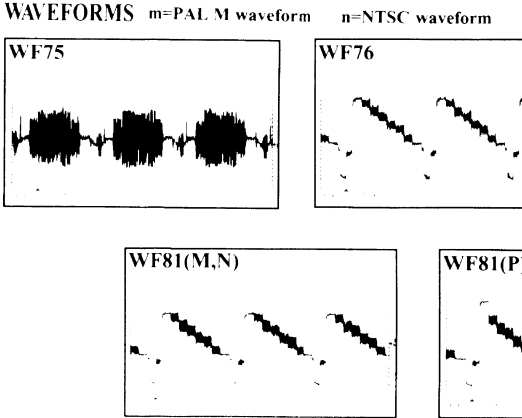
00.
S OTHERWISE
HERWISE
E PARTS LIST,
ED.

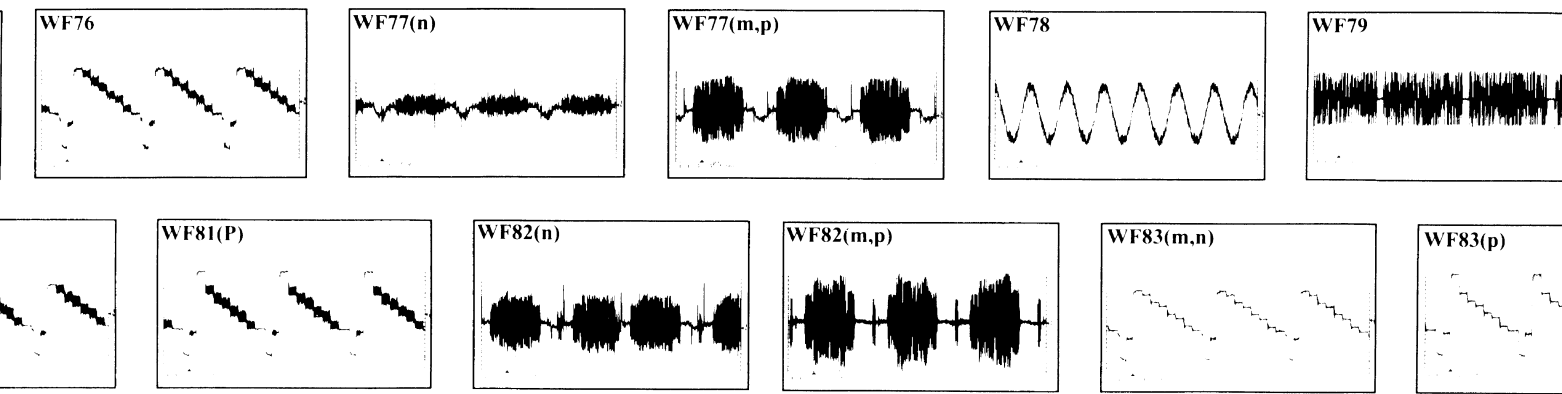
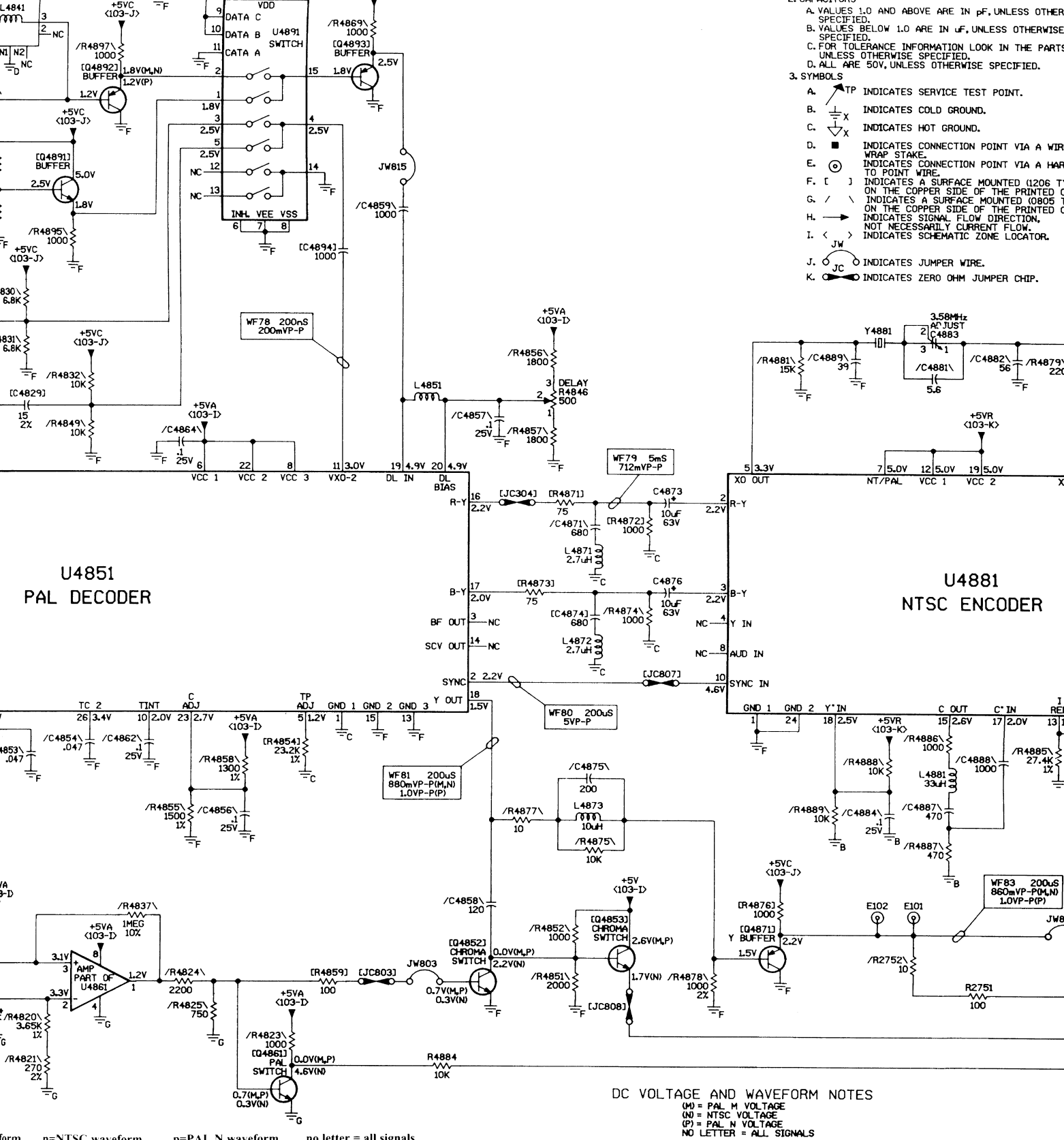
A WIRE
A HARD POINT

1206 TYPE) "CHIP"
INTED CIRCUIT BOARD.
0805 TYPE) "MINI CHIP"
INTED CIRCUIT BOARD.
ON,
TOR.

P.







CAPACITORS
 A. VALUES 1.0 AND ABOVE ARE IN μF , UNLESS OTHERWISE SPECIFIED.
 B. VALUES BELOW 1.0 ARE IN nF , UNLESS OTHERWISE SPECIFIED.
 C. FOR TOLERANCE INFORMATION LOOK IN THE PARTS LIST, UNLESS OTHERWISE SPECIFIED.
 D. ALL ARE 50V, UNLESS OTHERWISE SPECIFIED.

SYMBOLS

A.  TP INDICATES SERVICE TEST POINT.

INDICATES COLD GROUND

B. $\frac{1}{\text{---}} \times$ INDICATES COLD GROUND.

C. INDICATES HOT GROUND.

D. ■ INDICATES CONNECTION POINT VIA A WIRE

WRAP STAKE.
INDICATES CONNECTION POINT VIA A HARD POINT

E.  INDICATES CONNECTION POINT VIA A HARD POINT TO POINT WIRE.

F. [] INDICATES A SURFACE MOUNTED (1206 TYPE) "CHIP"
ON THE COPPER SIDE OF THE PRINTED CIRCUIT BOARD

G. / \ INDICATES A SURFACE MOUNTED (0805 TYPE) "MINI CHIP"

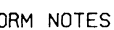
ON THE COPPER SIDE OF THE PRINTED CIRCUIT BOARD.
INDICATES SIGNAL FLOW DIRECTION

H. $\longrightarrow$ INDICATES SIGNAL FLOW DIRECTION,
NOT NECESSARILY CURRENT FLOW.

I. $\langle \quad \rangle$ INDICATES SCHEMATIC ZONE



J. $\bigcirc_{JC}$ $\bigcirc$ INDICATES JUMPER WIRE.



RESULTS

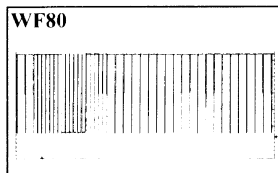
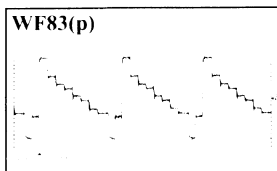
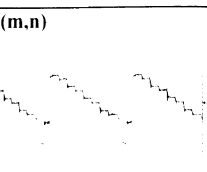
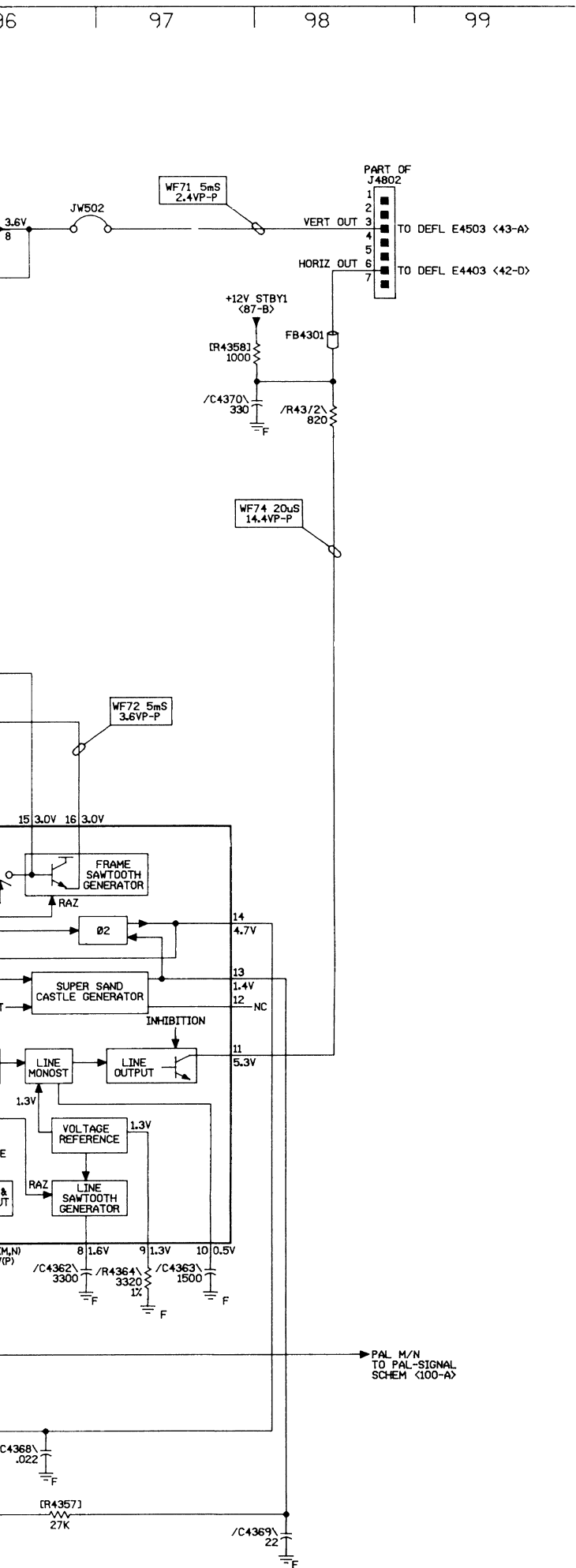
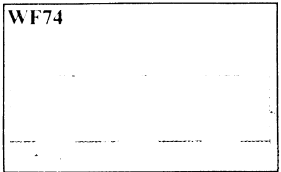
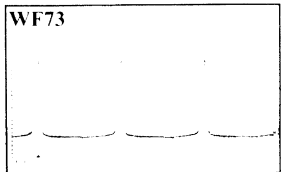
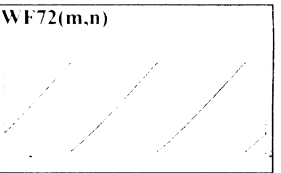
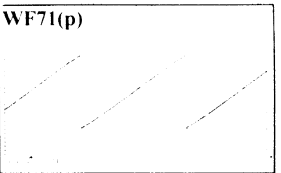
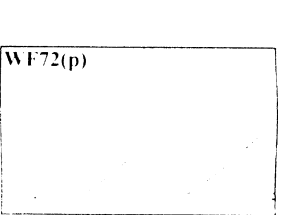
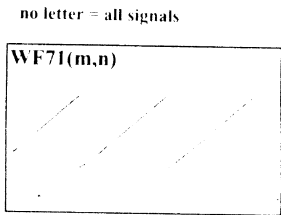
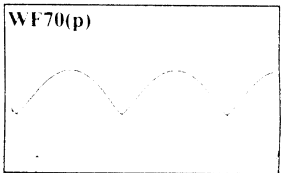
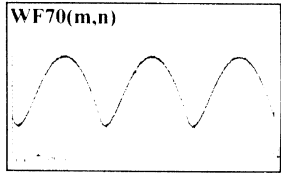


DIAGRAMA ESQUEMÁTICO DE DEFLEXÃO DE PAL



WAVEFORMS

m=PAL M waveform n=NTSC waveform p=PAL N waveform



90

91

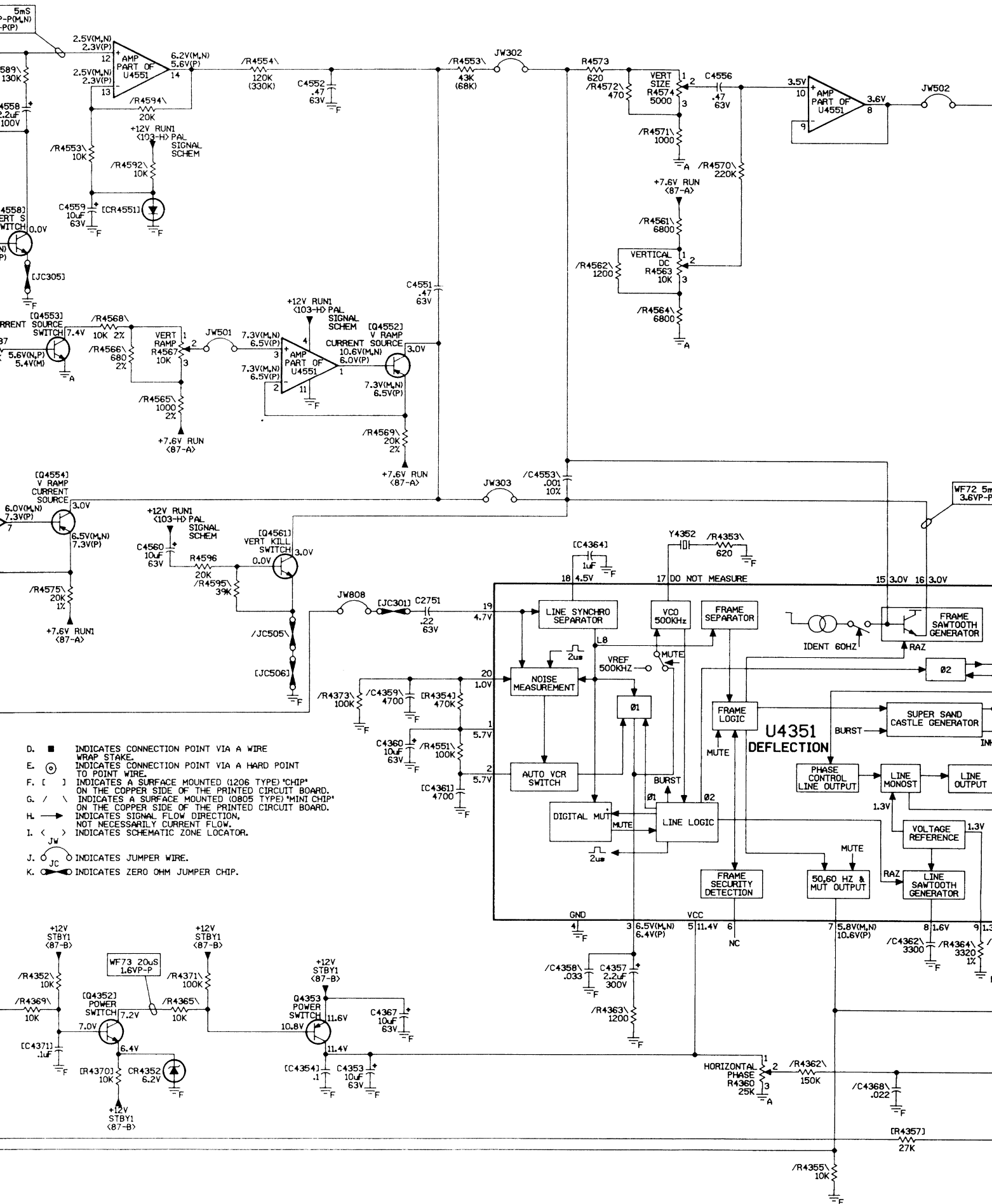
92

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96



Placa de Pal

FPA CONTROL SCHEMATIC

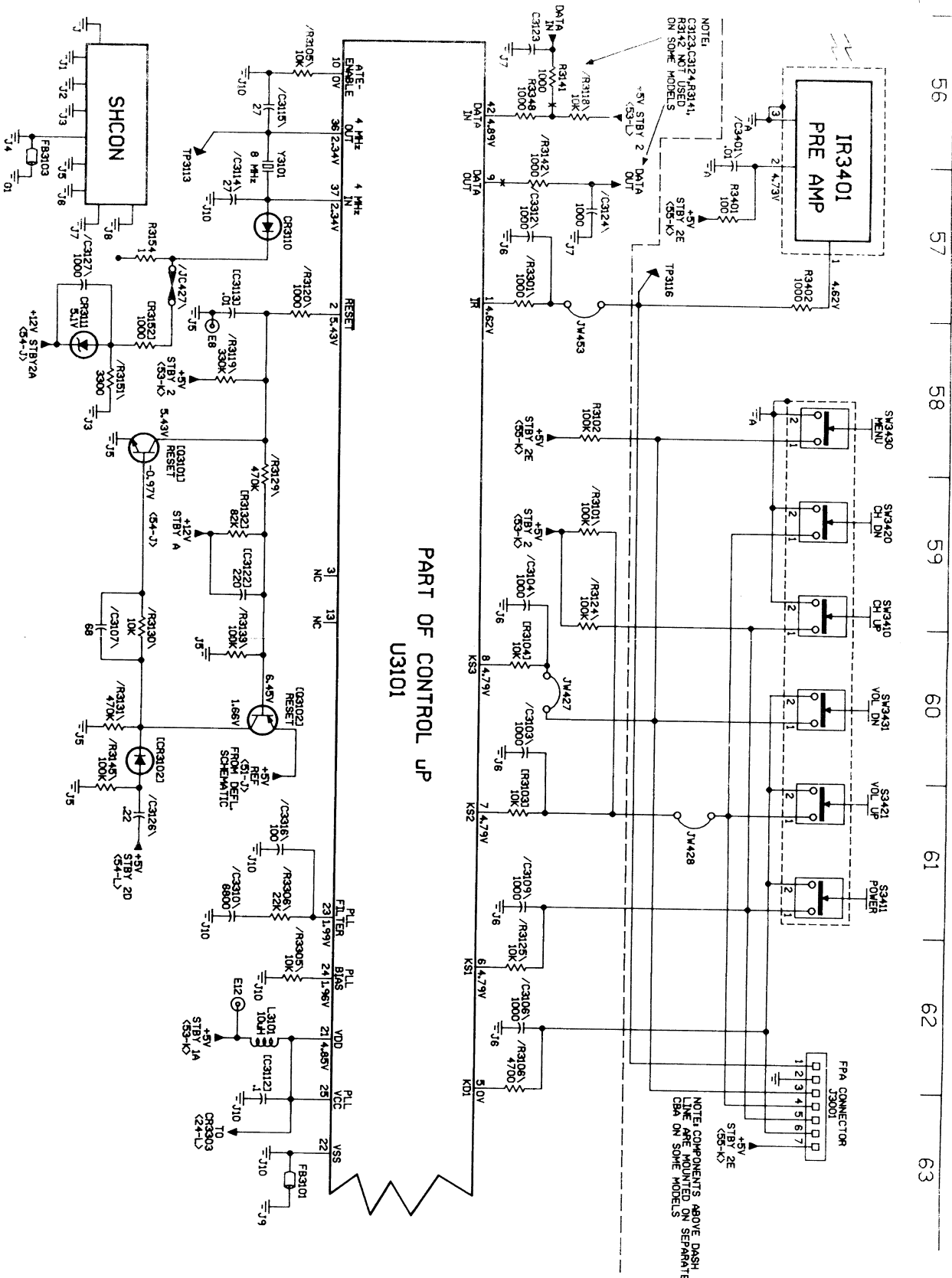
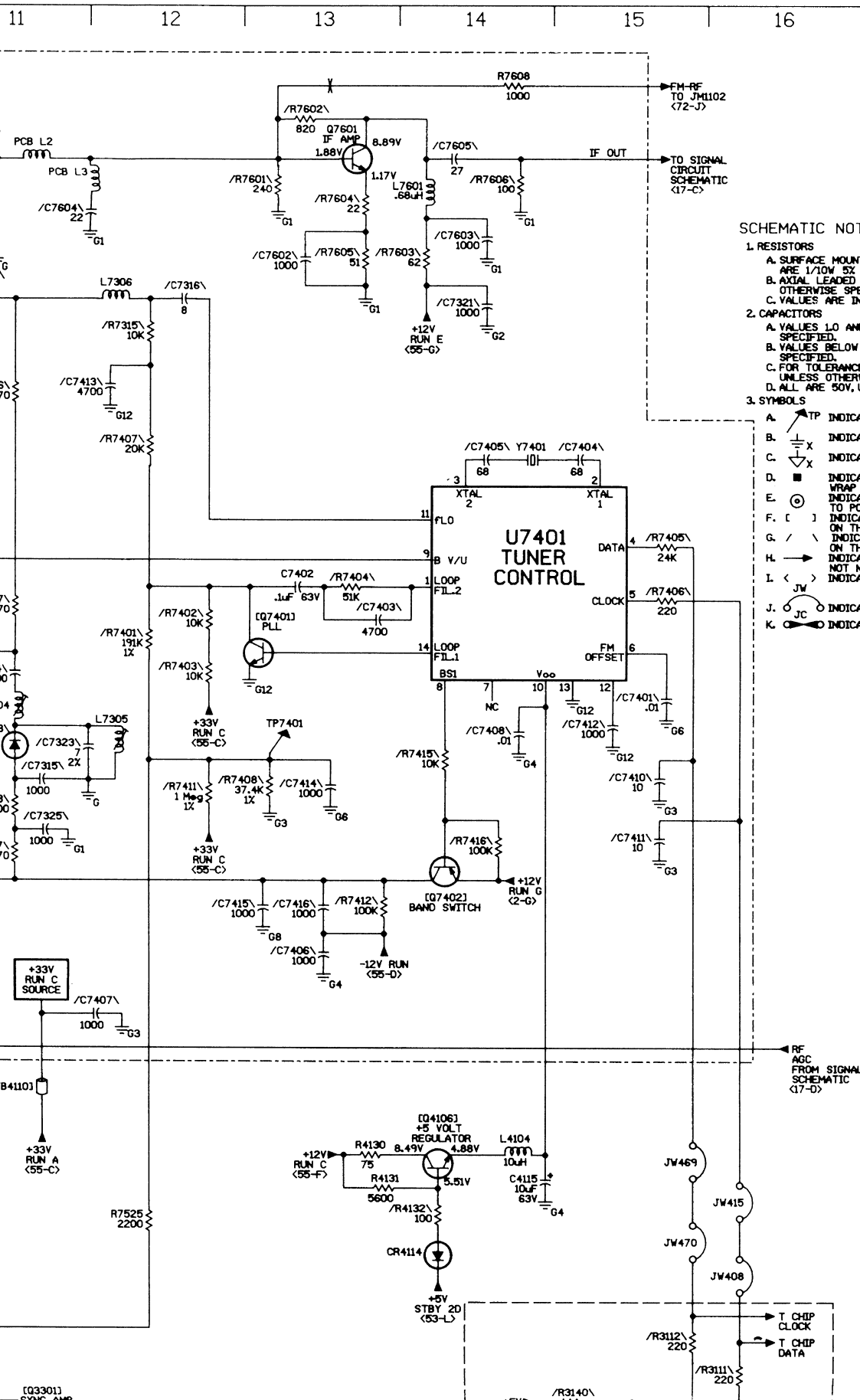


DIAGRAMA ESQUEMÁTICO DO TUNER



SCHEMATIC NOTES

1. RESISTORS

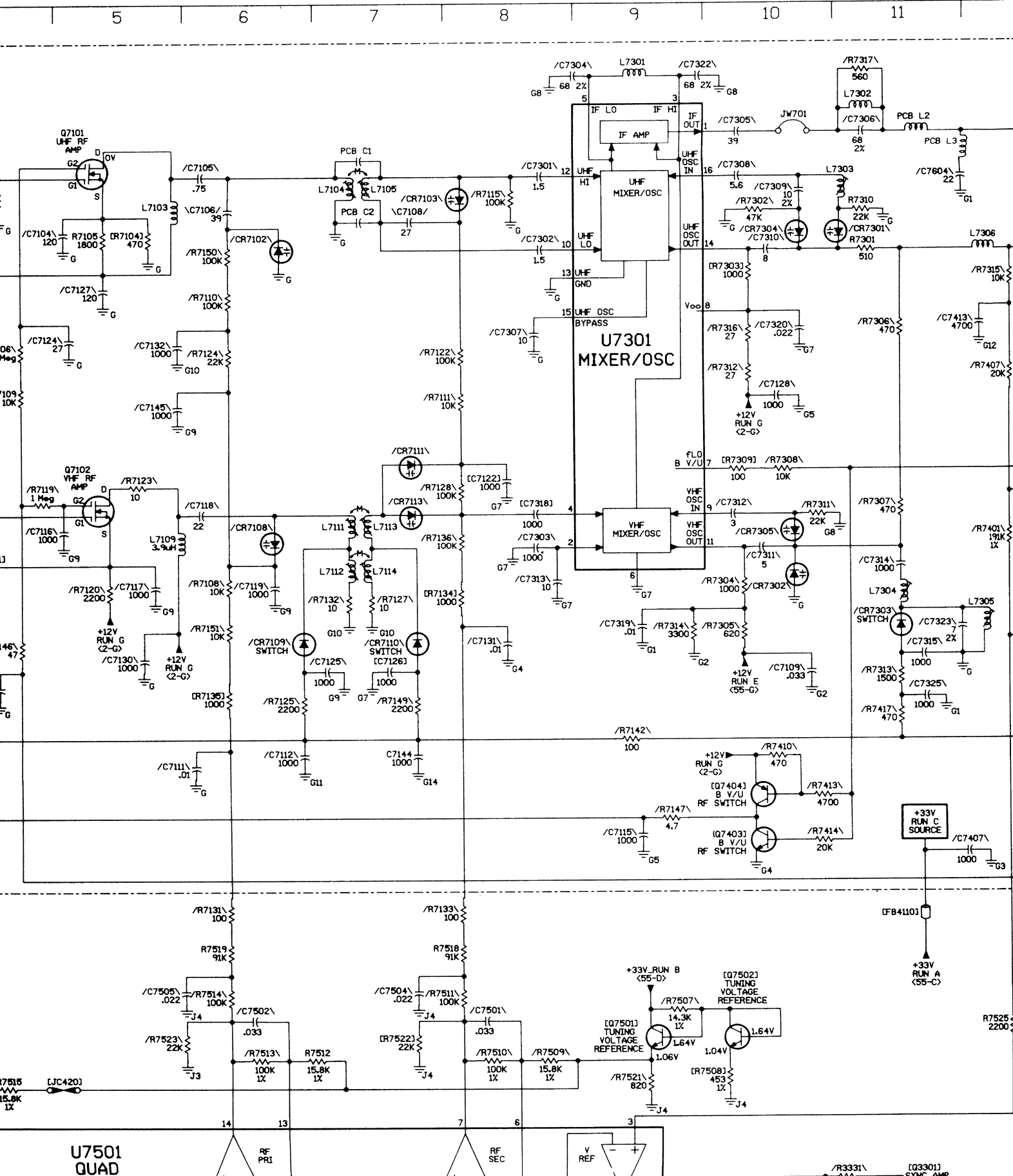
- A. SURFACE MOUNTED 1206'S ARE 1/8W, 5% TOLERANCE, 0805'S ARE 1/10W, 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
- B. AXIAL LEADED ARE 1/4W, 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
- C. VALUES ARE IN OHMS, K=X1000, M=X1,000,000.

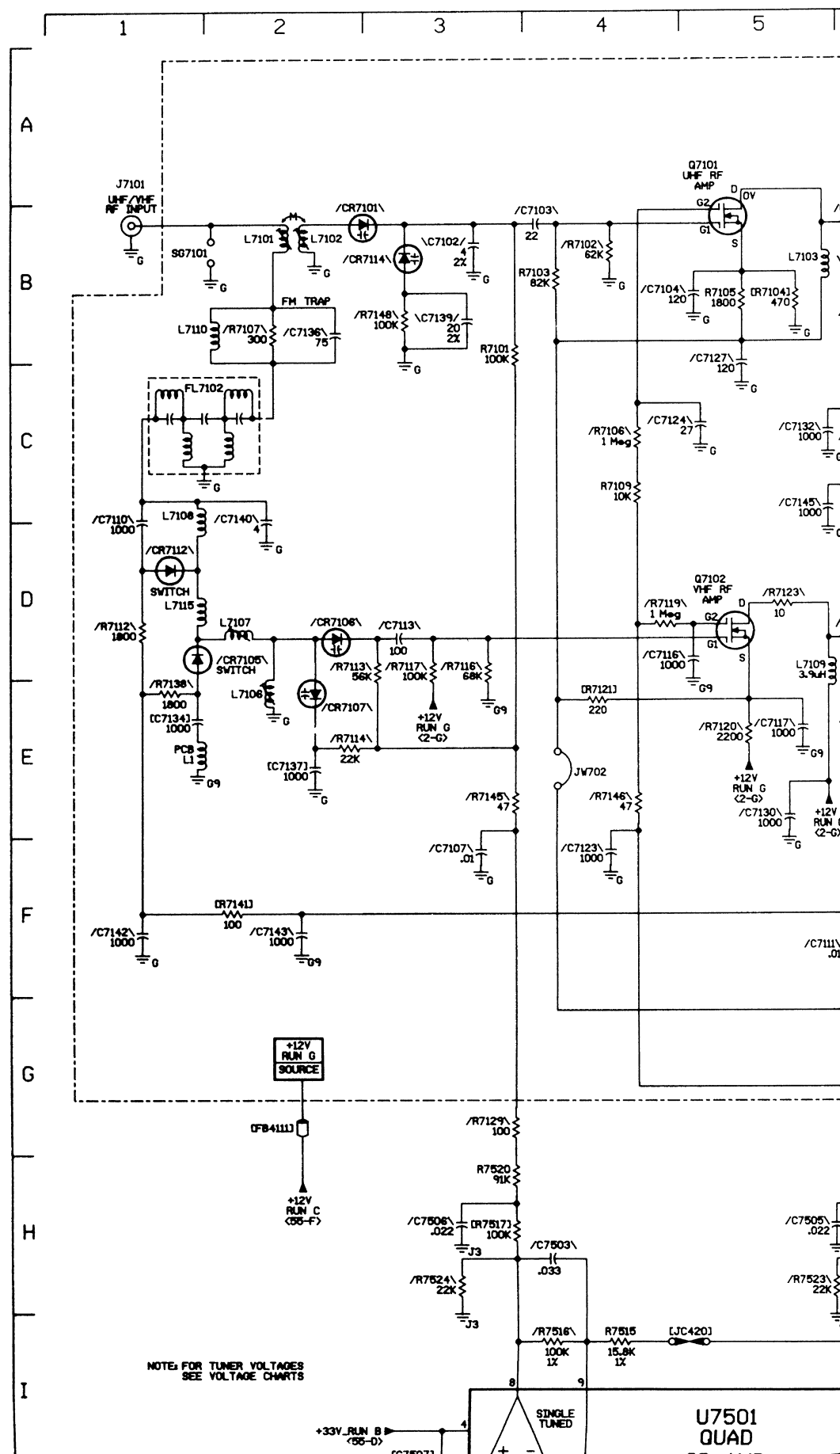
2. CAPACITORS

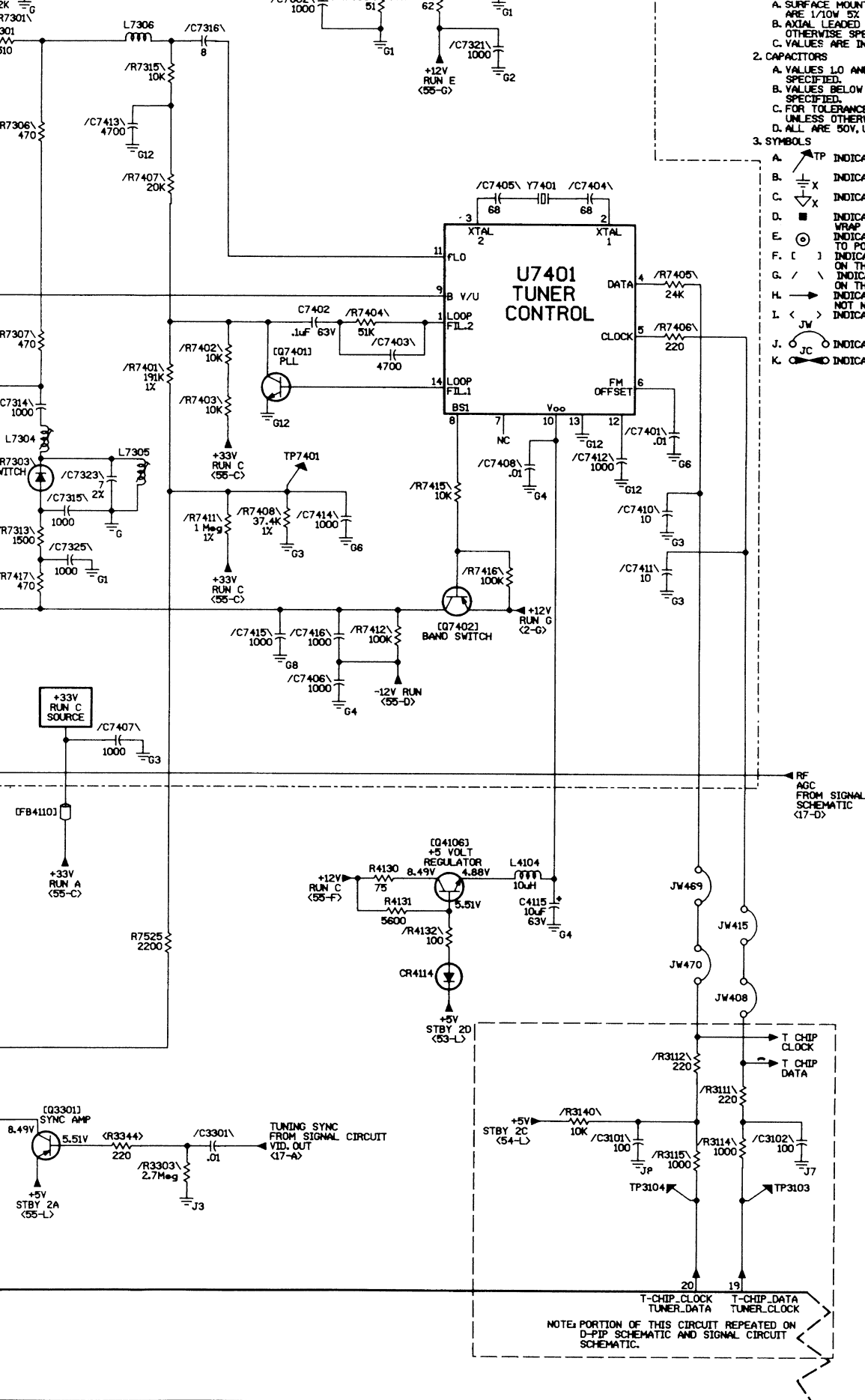
- A. VALUES 1.0 AND ABOVE ARE IN pF, UNLESS OTHERWISE SPECIFIED.
- B. VALUES BELOW 1.0 ARE IN uF, UNLESS OTHERWISE SPECIFIED.
- C. FOR TOLERANCE INFORMATION LOOK IN THE PARTS LIST, UNLESS OTHERWISE SPECIFIED.
- D. ALL ARE 50V, UNLESS OTHERWISE SPECIFIED.

3. SYMBOLS

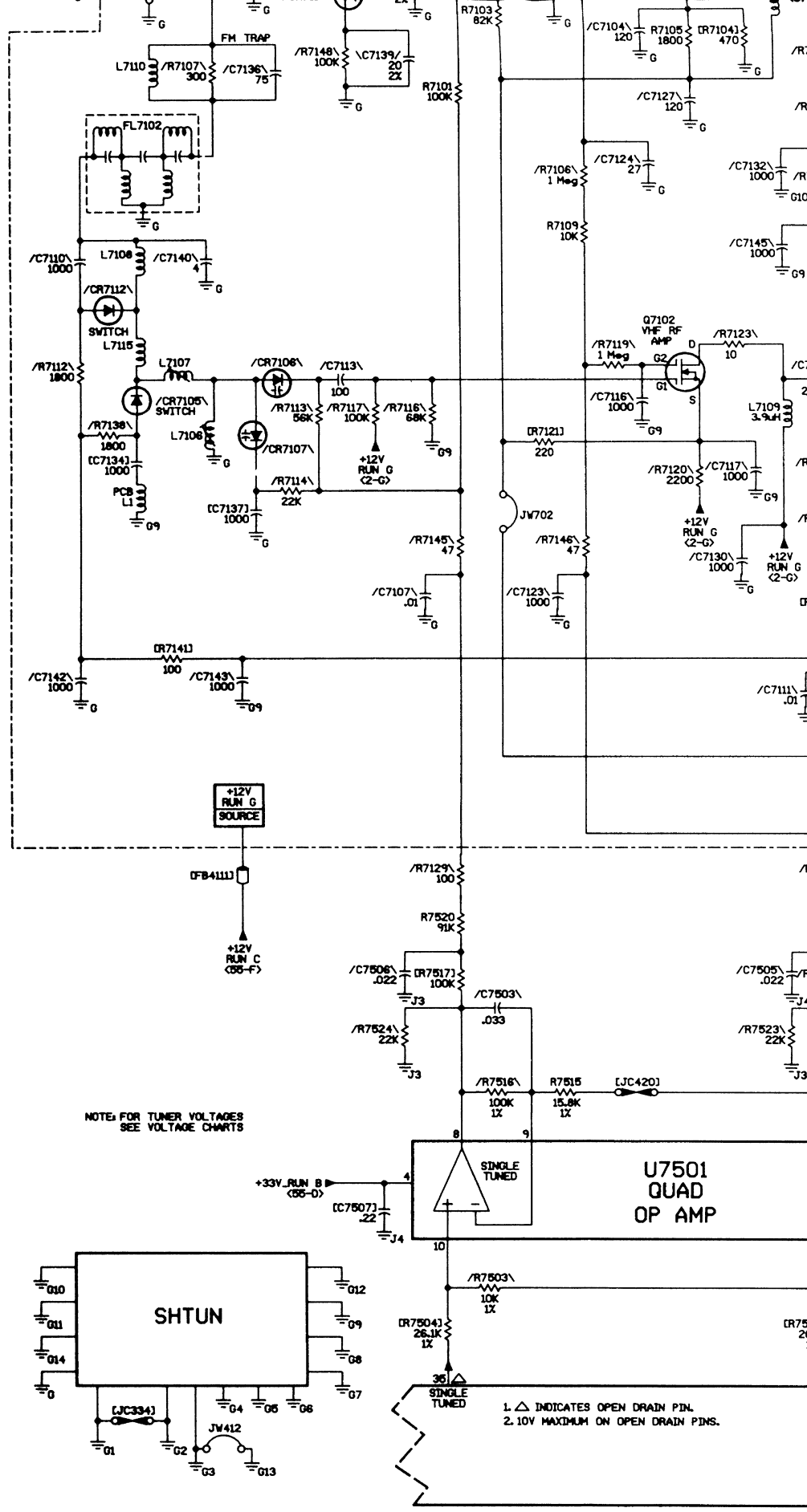
- A. TP INDICATES SERVICE TEST POINT.
- B. $\frac{1}{\infty}$ INDICATES COLD GROUND.
- C. $\frac{1}{\infty}$ INDICATES HOT GROUND.
- D. ■ INDICATES CONNECTION POINT VIA A WIRE WRAP STAKE.
- E. ⊙ INDICATES CONNECTION POINT VIA A HARD POINT TO POINT WIRE.
- F. [] INDICATES A SURFACE MOUNTED (1206 TYPE) "CHIP" ON THE COPPER SIDE OF THE PRINTED CIRCUIT BOARD.
- G. / \ INDICATES A SURFACE MOUNTED (0805 TYPE) "MINI CHIP" ON THE COPPER SIDE OF THE PRINTED CIRCUIT BOARD.
- H. → INDICATES SIGNAL FLOW DIRECTION, NOT NECESSARILY CURRENT FLOW.
- I. < JW INDICATES SCHEMATIC ZONE LOCATOR.
- J. JC INDICATES JUMPER WIRE.
- K. ○ INDICATES ZERO OHM JUMPER CHIP.







B
C
D
E
F
G
H
I
J
K



NOTE: FOR TUNER VOLTAGES
SEE VOLTAGE CHARTS

1. Δ INDICATES OPEN DRAIN PIN.
2. 10V MAXIMUM ON OPEN DRAIN PINS.

DIAGRAMA ESQUEMÁTICO DO SINAL

1 700mV p.p. 5mS per Div

2 50mV p.p. 5mS per Div

3 1.2V p.p. 20µS per Div

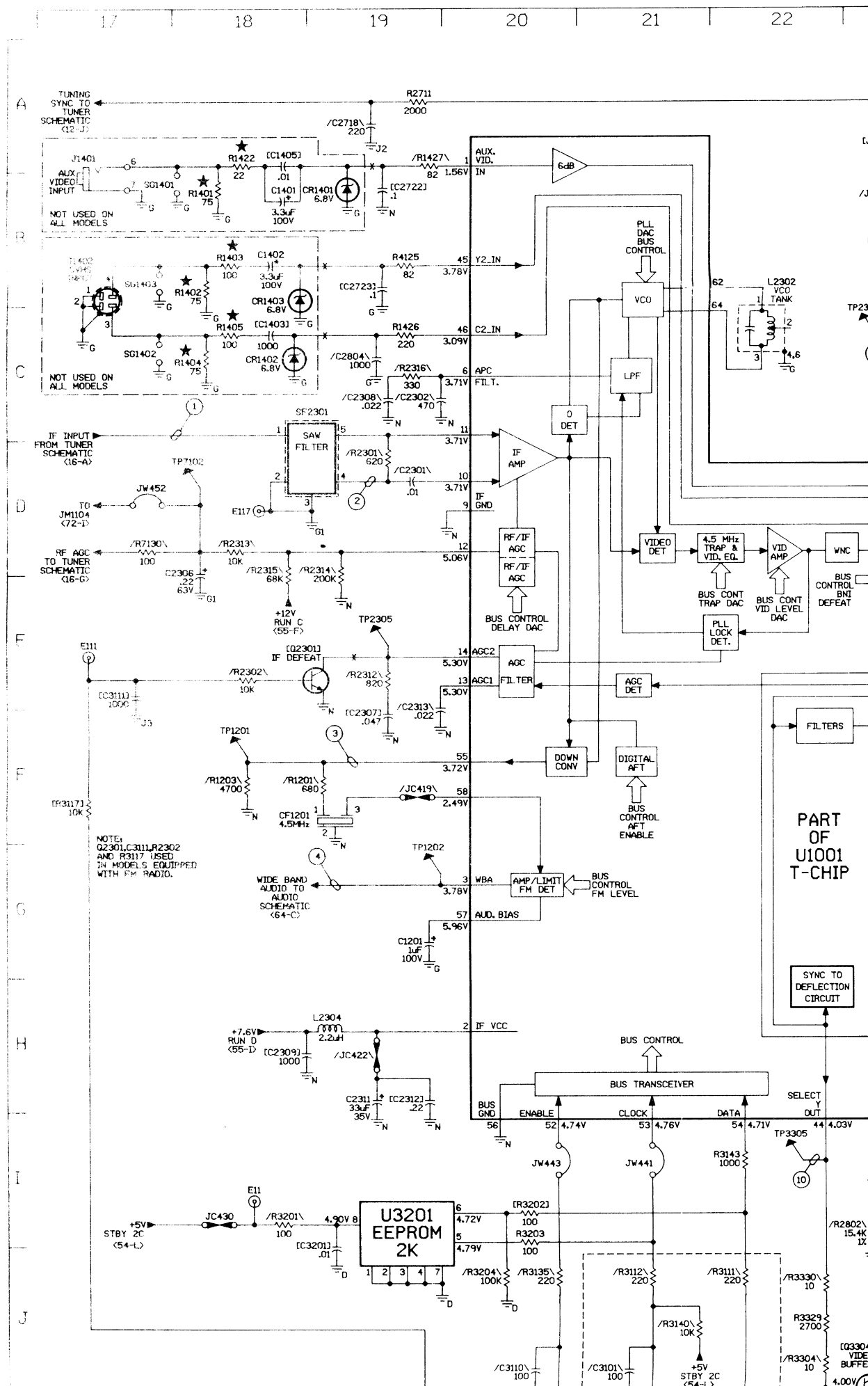
4 1.0V p.p. 5mS per Div

5 1.5V p.p. 20µS per Div

6 2.0V p.p. 20µS per Div

7 1.2V p.p. 20µS per Div

8 1.2V p.p. 20µS per Div



22

23

24

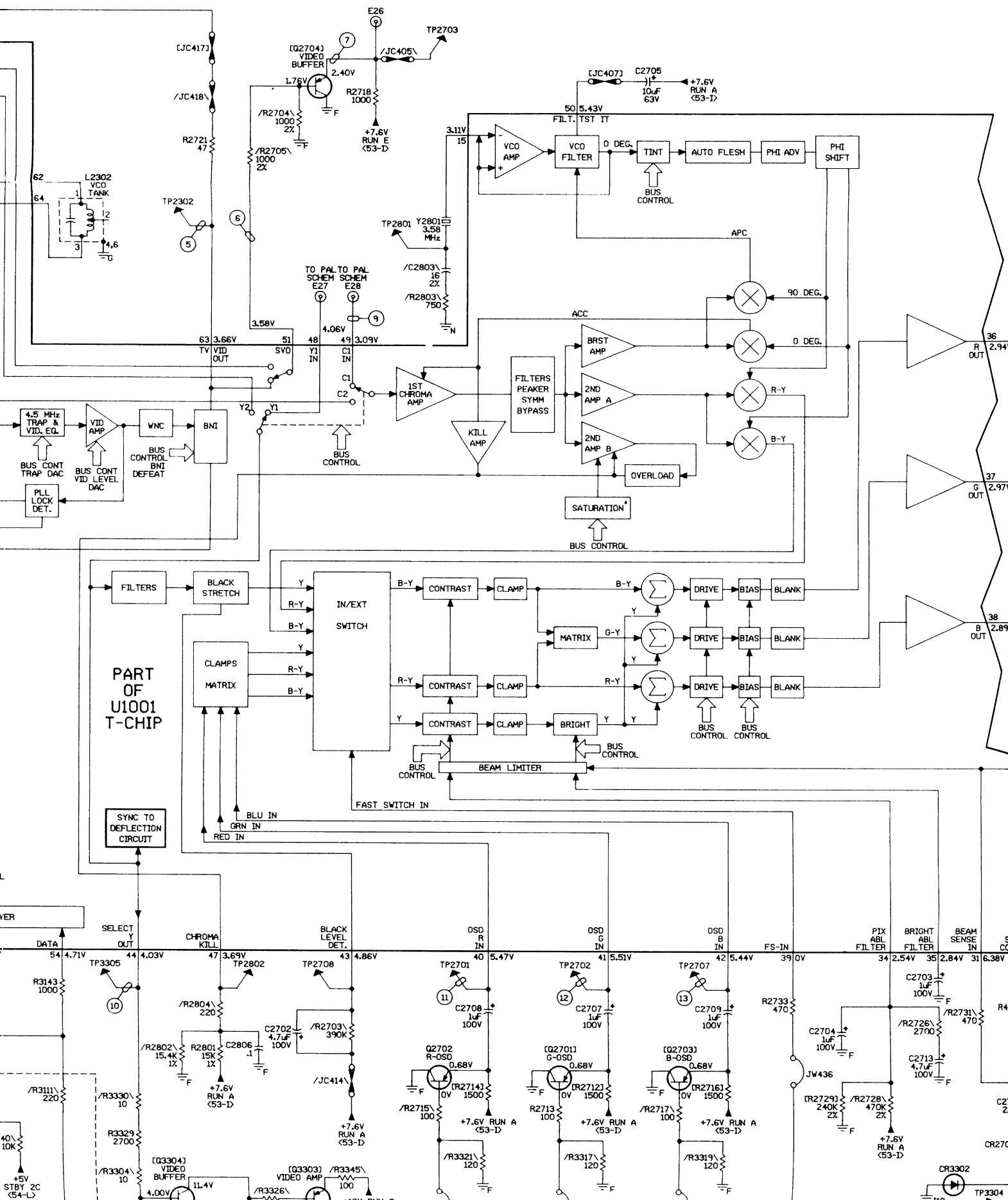
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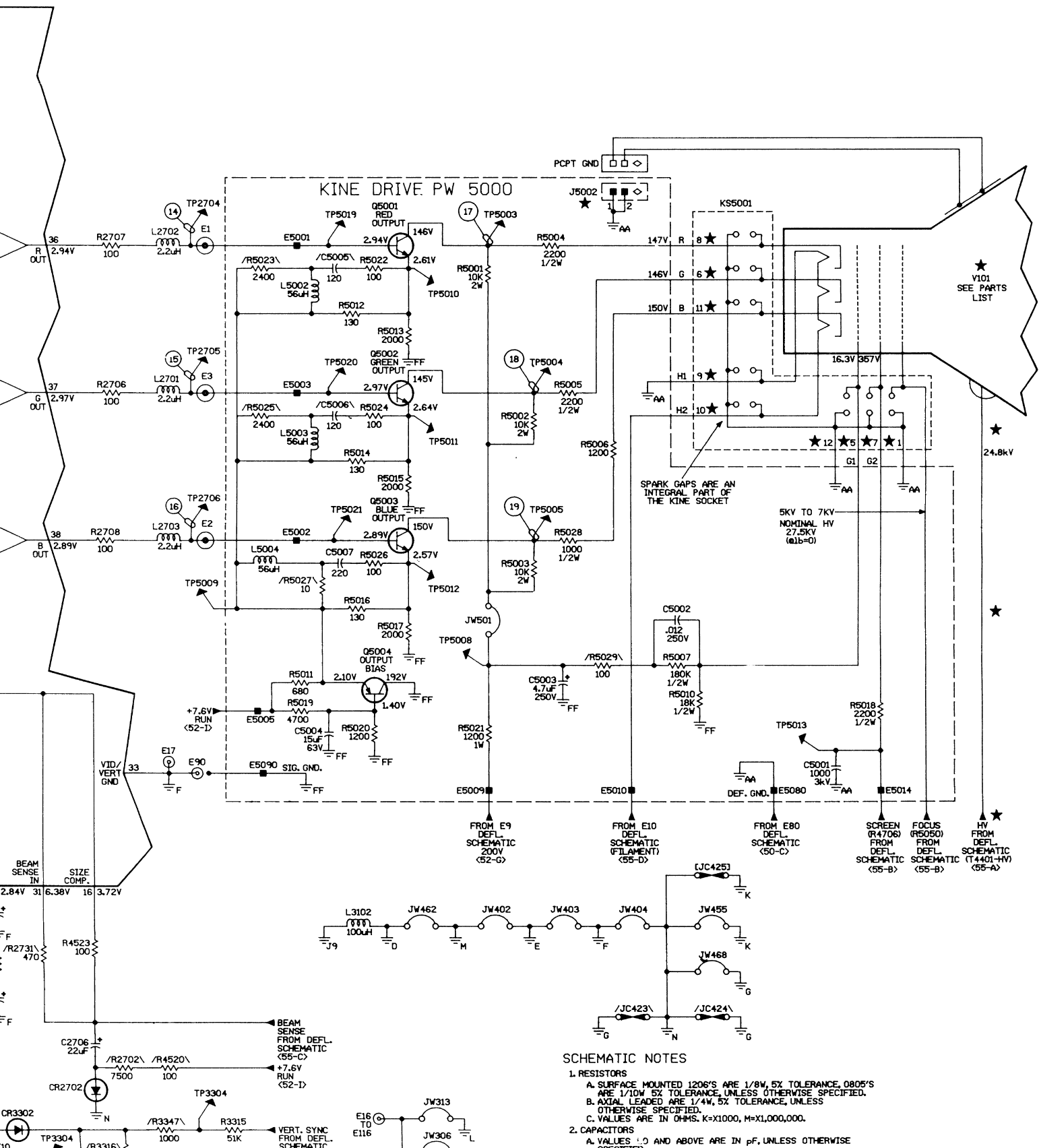
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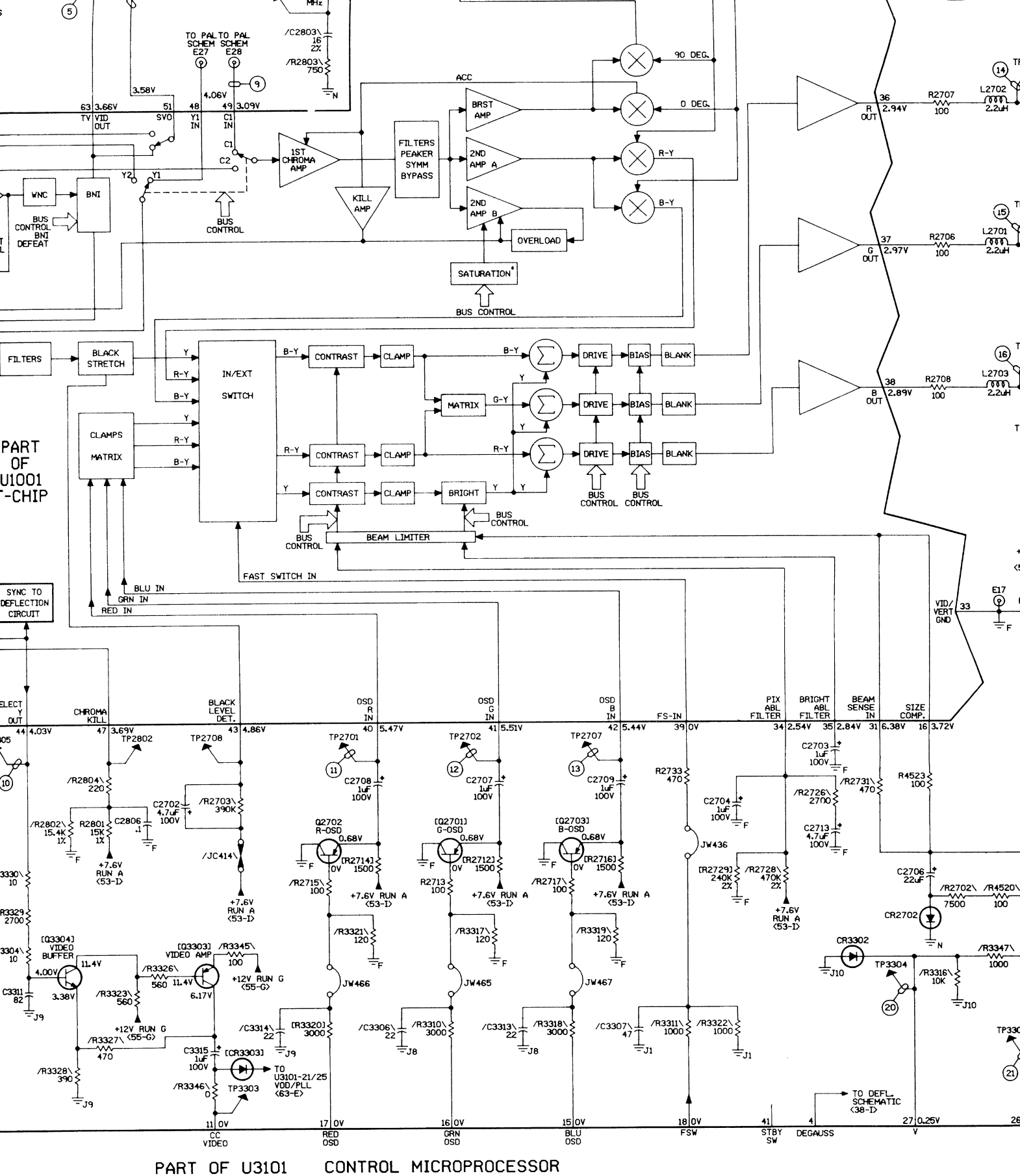
27

28

29







(16) 3.0V p-p 10μs per Div.

(17) 125V p-p 20μs per Div.

(18) 125V p-p 20μs per Div.

(19) 120V p-p 20μs per Div.

(20) 5.0V p-p 5ms per Div.

(21)

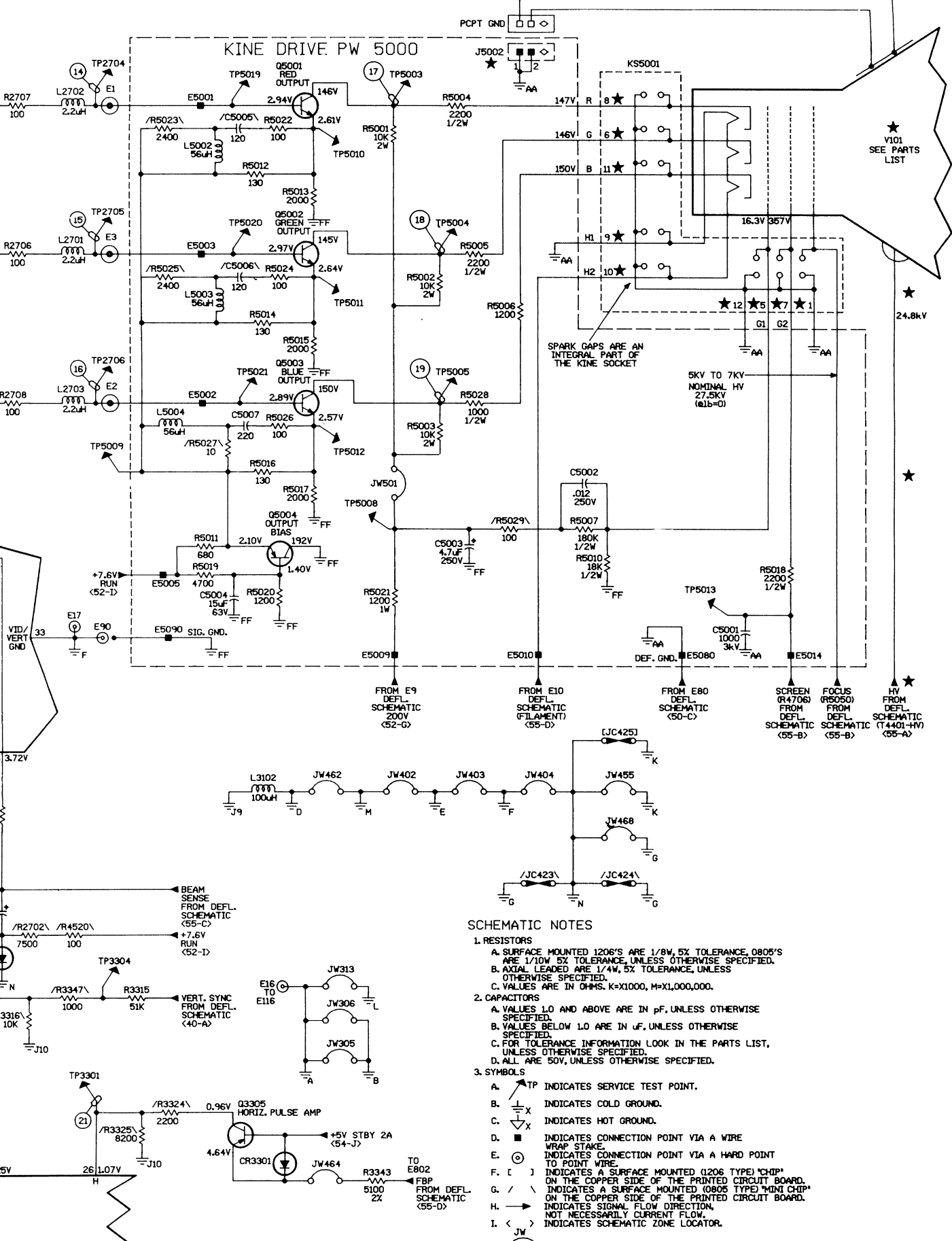
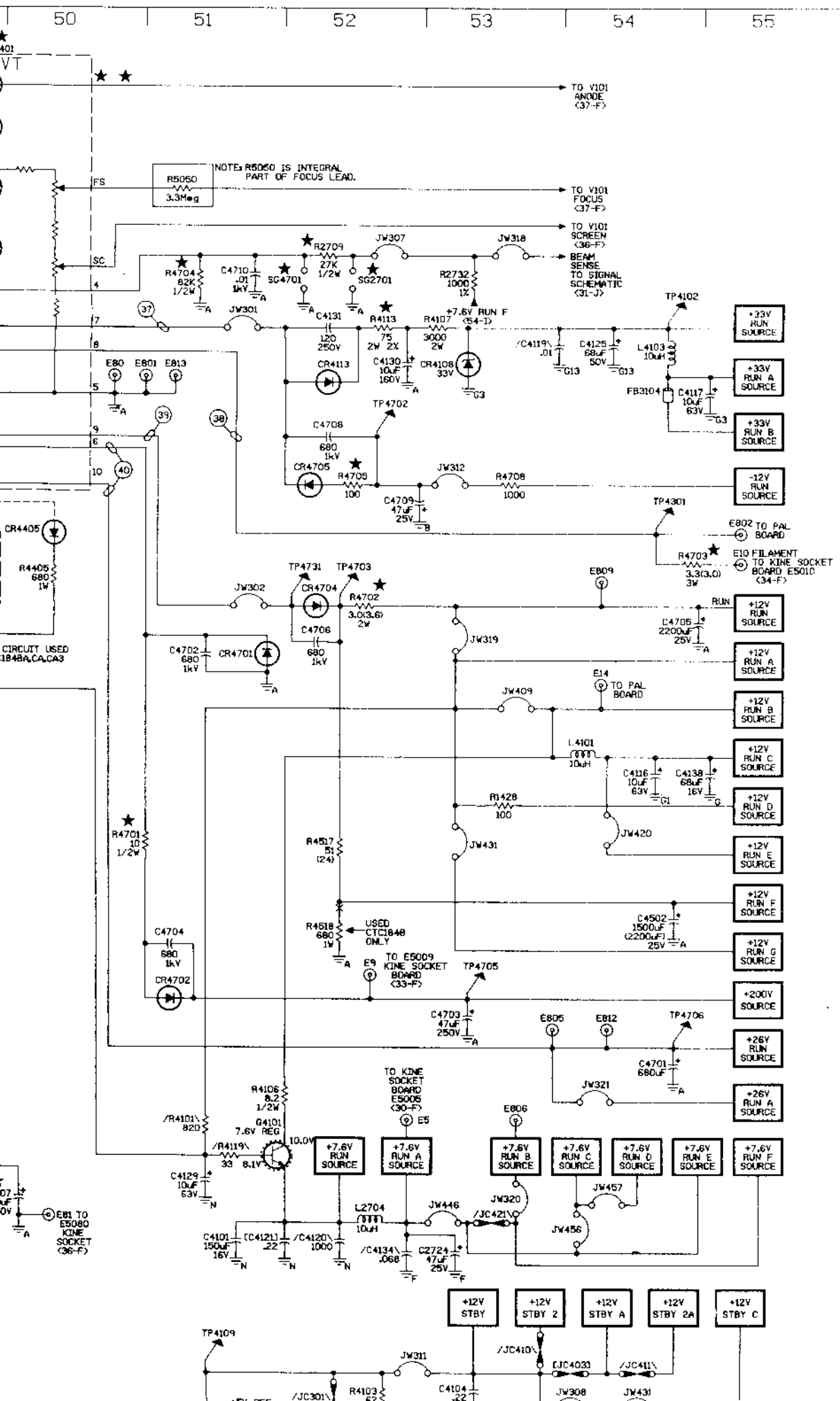


DIAGRAMA ESQUEMÁTICO DE DEFLEXÃO/FORÇA



(42) 100V p-p 5uS per Div
Run Mode

(42) 100V p-p 5uS per Div
Off Mode

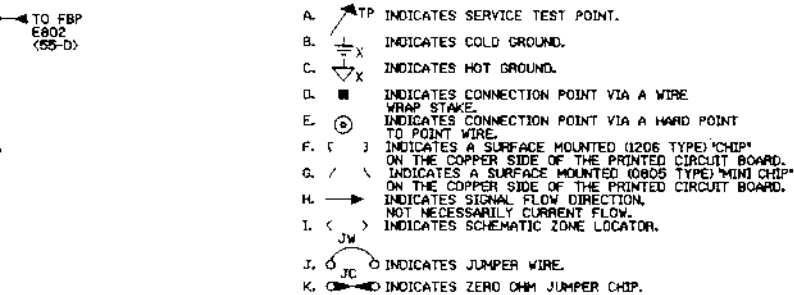
(43) 22.5V p-p 5uS per Div
Run Mode

(43) 22.5V p-p 10uS per Div
Off Mode

(44) 100V p-p 5uS per Div
Run Mode

(44) 100V p-p 10uS per Div
Off Mode

(45) 100V p-p 5uS per Div
Run Mode

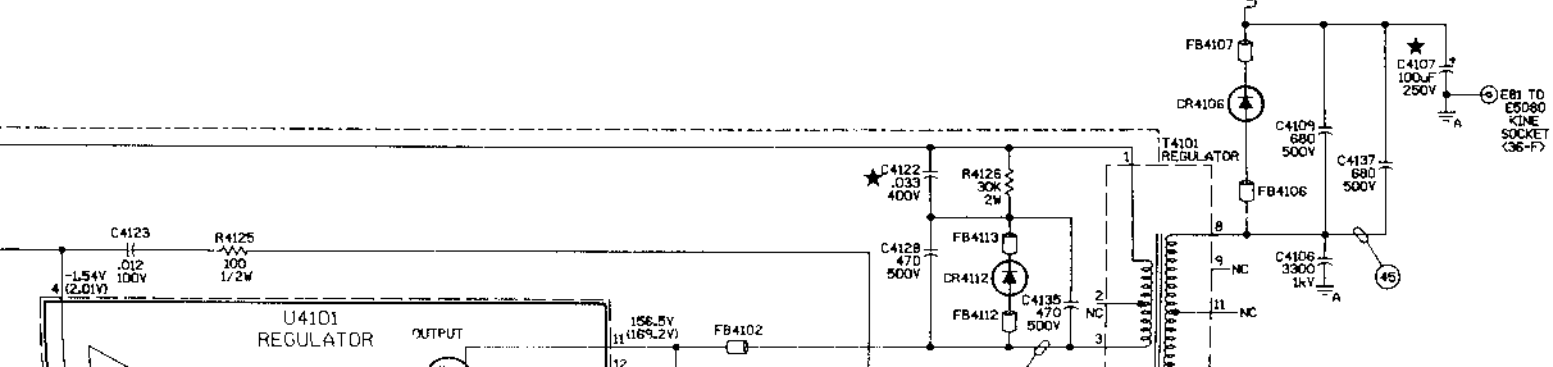


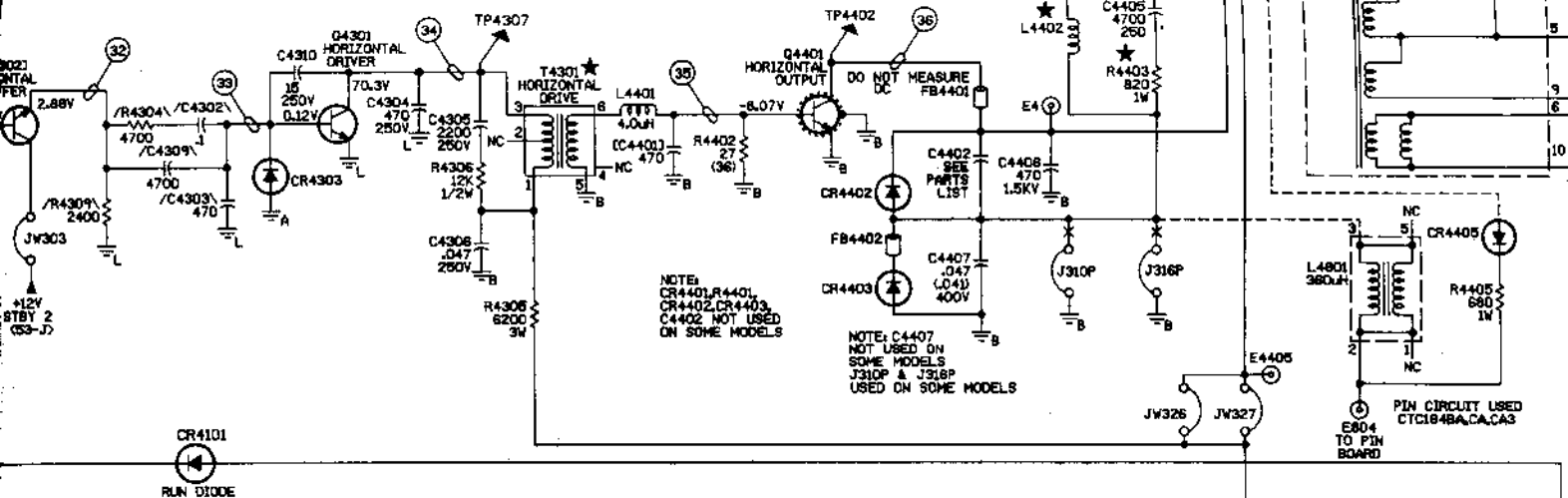
1. RESISTORS

- A. SURFACE MOUNTED 1206'S ARE 1/8W, 5% TOLERANCE, 0805'S ARE 1/10W, 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
 - B. AXIAL LEADED ARE 1/4W, 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
 - C. VALUES ARE IN OHMS, K=X1000, M=X1,000,000.
2. CAPACITORS
- A. VALUES 1.0 AND ABOVE ARE IN pF, UNLESS OTHERWISE SPECIFIED.
 - B. VALUES BELOW 1.0 ARE IN fF, UNLESS OTHERWISE SPECIFIED.
 - C. FOR TOLERANCE INFORMATION LOOK IN THE PARTS LIST, UNLESS OTHERWISE SPECIFIED.
 - D. ALL ARE 50V, UNLESS OTHERWISE SPECIFIED.

- ### 3. SYMBOLS

- A.  INDICATES SERVICE TEST POINT.
- B.  INDICATES COLD GROUND.
- C.  INDICATES HOT GROUND.
- D.  INDICATES CONNECTION POINT VIA A WIRE WRAP STAKE.
- E.  INDICATES CONNECTION POINT VIA A HARD POINT TO POINT WIRE.
- F.  INDICATES A SURFACE MOUNTED (1206 TYPE) "CHIP" ON THE COPPER SIDE OF THE PRINTED CIRCUIT BOARD.
- G.  INDICATES A SURFACE MOUNTED (0805 TYPE) "MINI" CHIP ON THE COPPER SIDE OF THE PRINTED CIRCUIT BOARD.
- H.  INDICATES SIGNAL FLOW DIRECTION, NOT NECESSARILY CURRENT FLOW.
- I.  INDICATES SCHEMATIC ZONE LOCATOR.
- J.  INDICATES JUMPER WIRE.
- K.  INDICATES ZERO OHM JUMPER CHIP.





SCHEMATIC NOTES

1. RESISTORS

- A. SURFACE MOUNTED 1206'S ARE 1/8W, 5% TOLERANCE, 0805'S ARE 1/10W 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
- B. AXIAL LEADED ARE 1/4W, 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
- C. VALUES ARE IN OHMS, K=1,000, M=1,000,000.

2. CAPACITORS

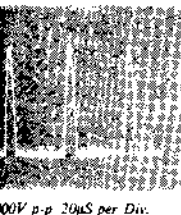
- A. VALUES 1.0 AND ABOVE ARE IN μ F, UNLESS OTHERWISE SPECIFIED.
- B. VALUES BELOW 1.0 ARE IN n F, UNLESS OTHERWISE SPECIFIED.
- C. FOR TOLERANCE INFORMATION LOOK IN THE PARTS LIST, UNLESS OTHERWISE SPECIFIED.
- D. ALL ARE 50V, UNLESS OTHERWISE SPECIFIED.

3. SYMBOLS

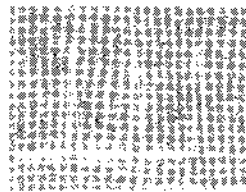
- A. TP INDICATES SERVICE TEST POINT.
- B. $\text{---} \perp \text{---}$ INDICATES COLD GROUND.
- C. $\text{---} \nabla \text{---}$ INDICATES HOT GROUND.
- D. $\blacksquare$ INDICATES CONNECTION POINT VIA A WIRE WRAP STAKE.
- E. $\odot$ INDICATES CONNECTION POINT VIA A HARD POINT TO POINT WIRE.
- F. $\text{---} \text{---}$ INDICATES A SURFACE MOUNTED (1206 TYPE) "CHIP" ON THE COPPER SIDE OF THE PRINTED CIRCUIT BOARD.
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- J. $\text{---} \text{---}$ INDICATES JUMPER WIRE.
- K. $\text{---} \text{---}$ INDICATES ZERO OHM JUMPER CHIP.

TO FB
E802
(55-D)

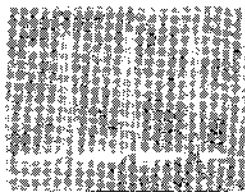
TO FB
E802
(55-D)



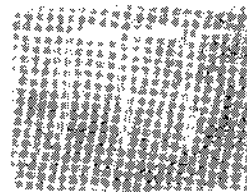
000V p-p 20 μ s per Div.



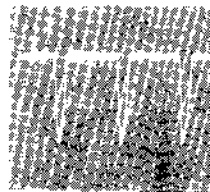
17 110V p-p 20 μ s per Div.



38 30V p-p 20 μ s per Div.



39 110V p-p 20 μ s per Div.



40 200V p-p 20 μ s per Div.

②⑤ 1.5V p-p 5mS per Div.

26 2.0V p-p 5mS per Div.

(27) 2.0V p-p SmS per Div.

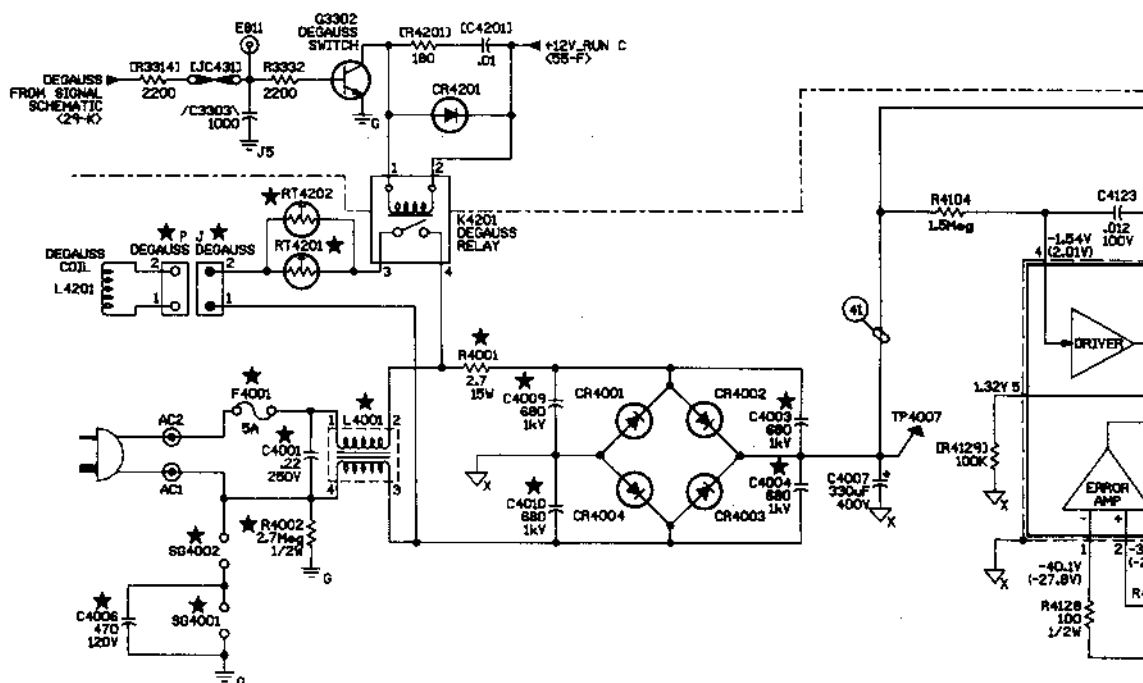
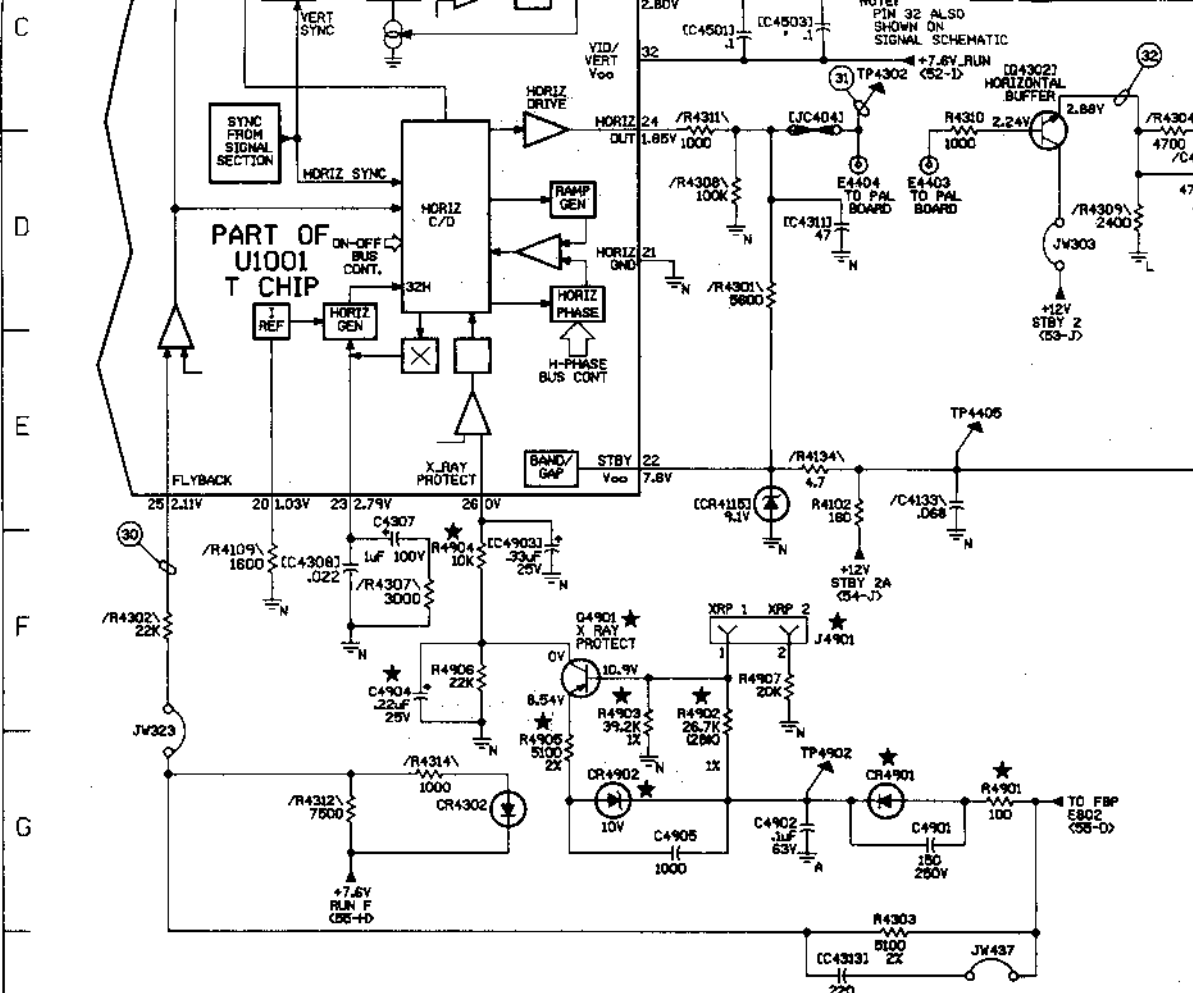
28 1.0V p-p 5mS per Div.

(29) 55V p-p 5mS per Div.

③⑩ 8.0V p-p 20μS per Div

(31) 7.0V p-p 20 μ S per Div

(32) 5.0V p-p 20 μ S per Div



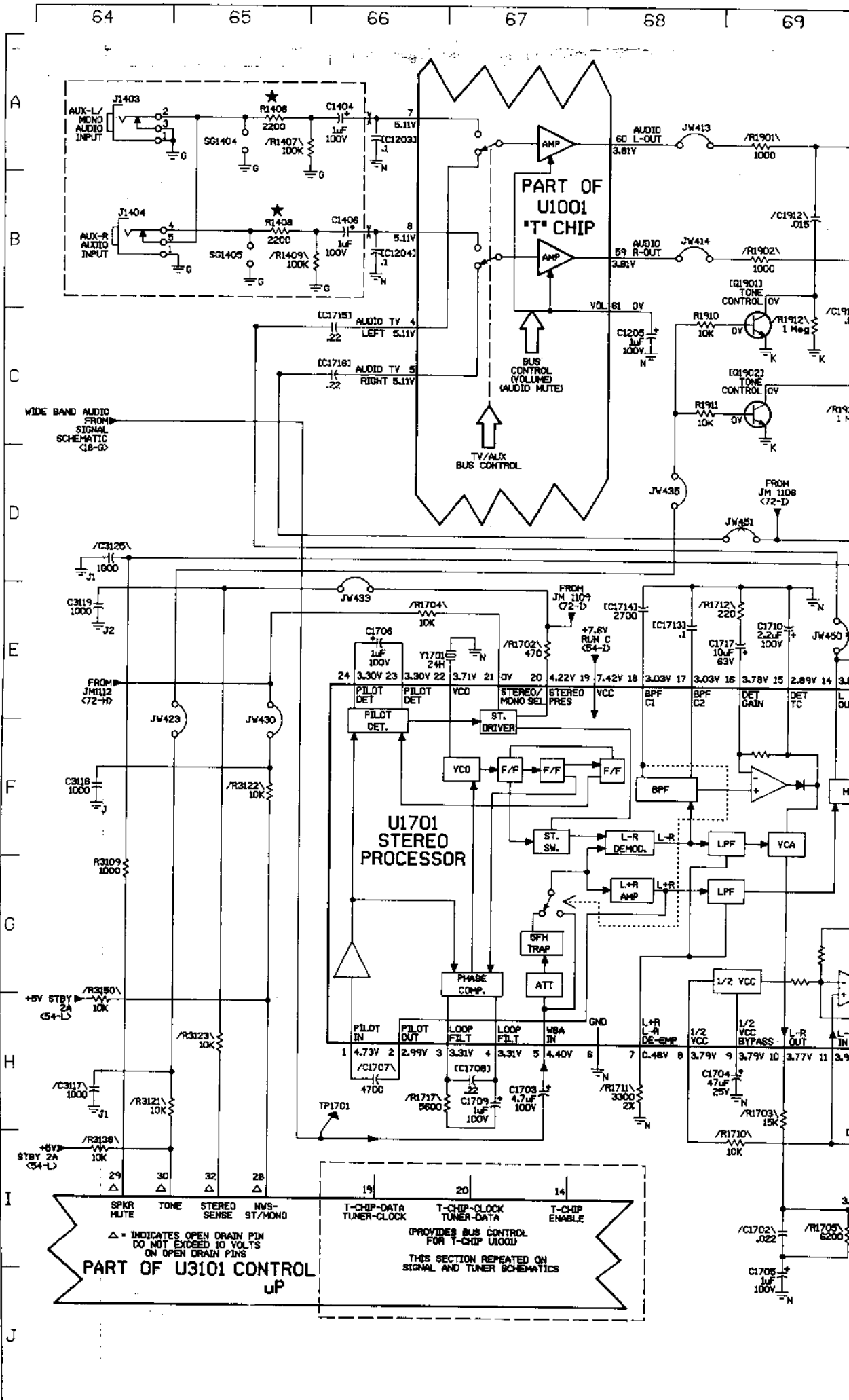
③③ 1.5V p-p 20μS per Div.

③ 1.25V p-p 20ns per div

③5 12V p-p 20μS per Div

36 1000V d.p. 20μS p.p.s.

DIAGRAMA STEREO



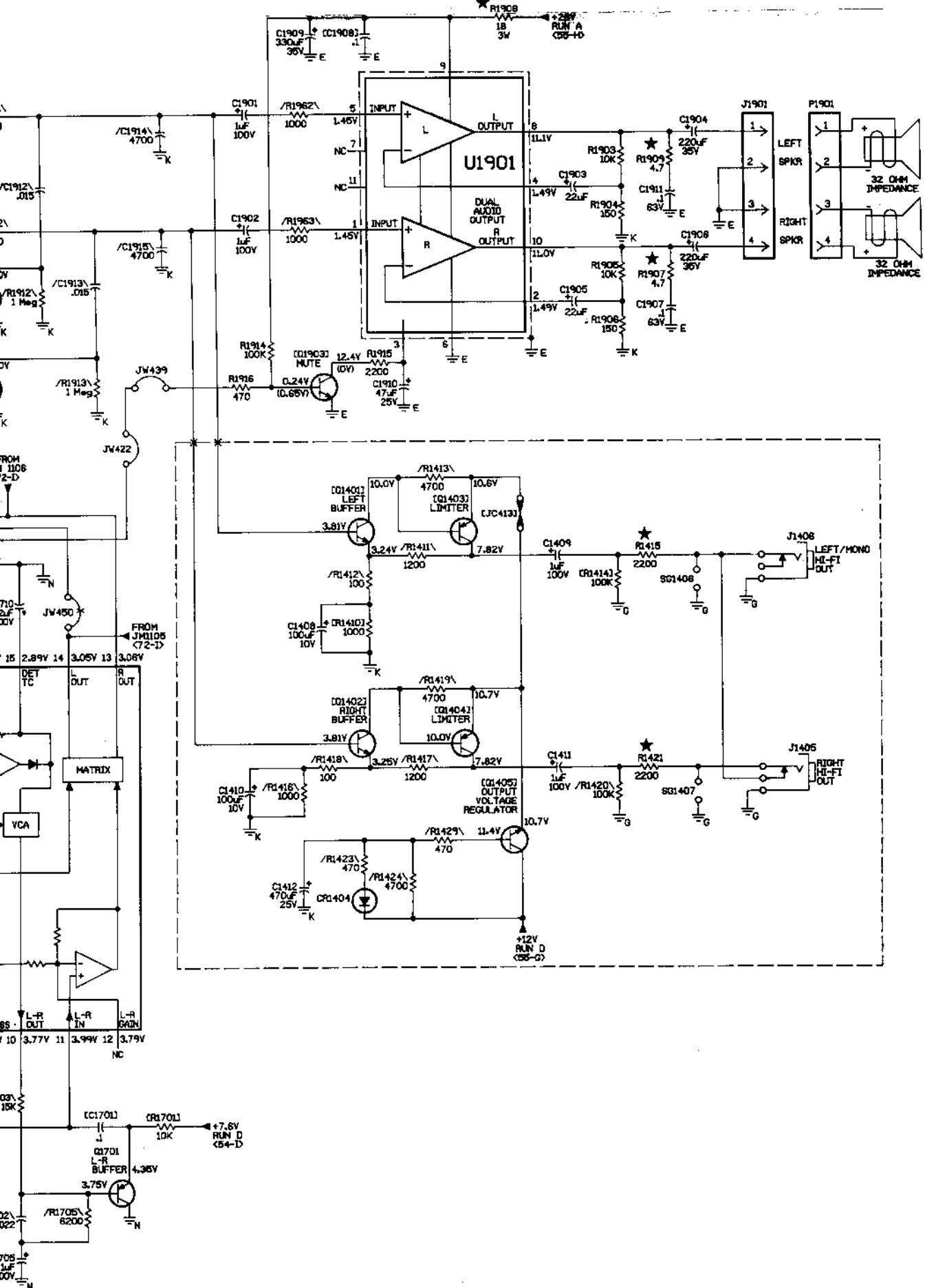
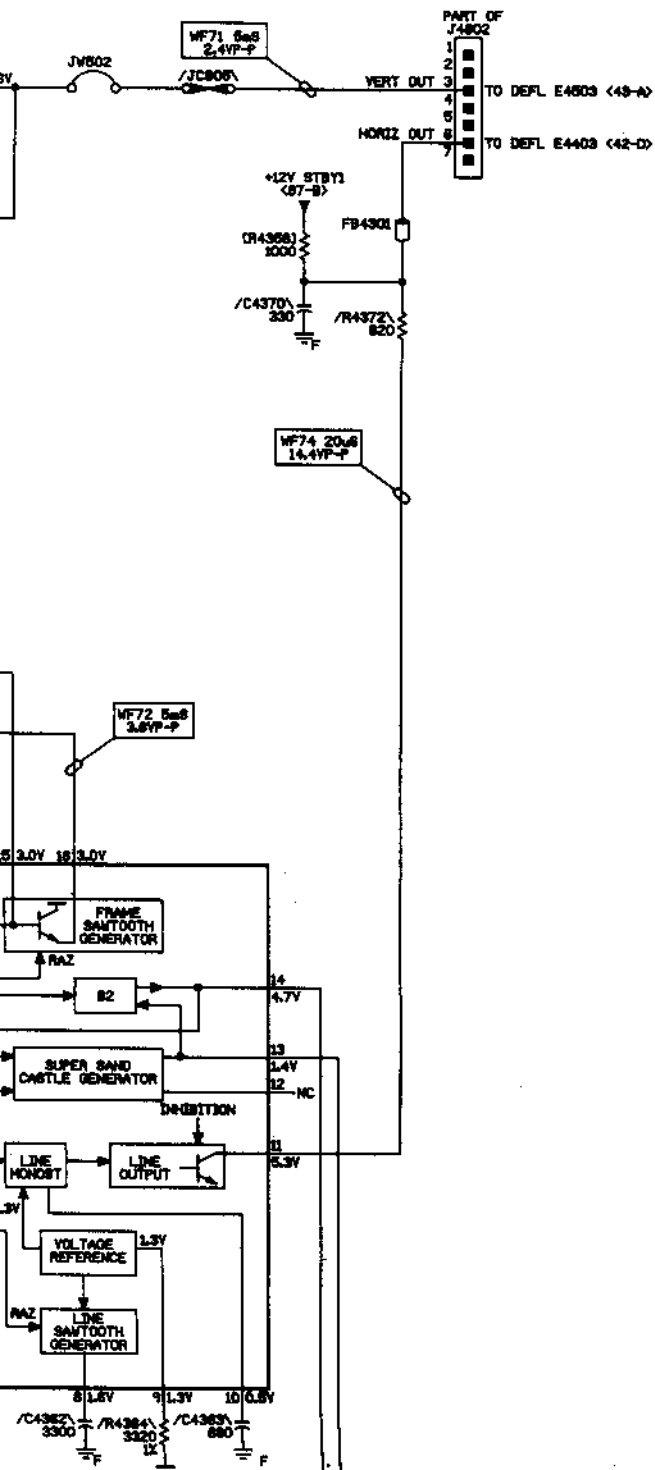
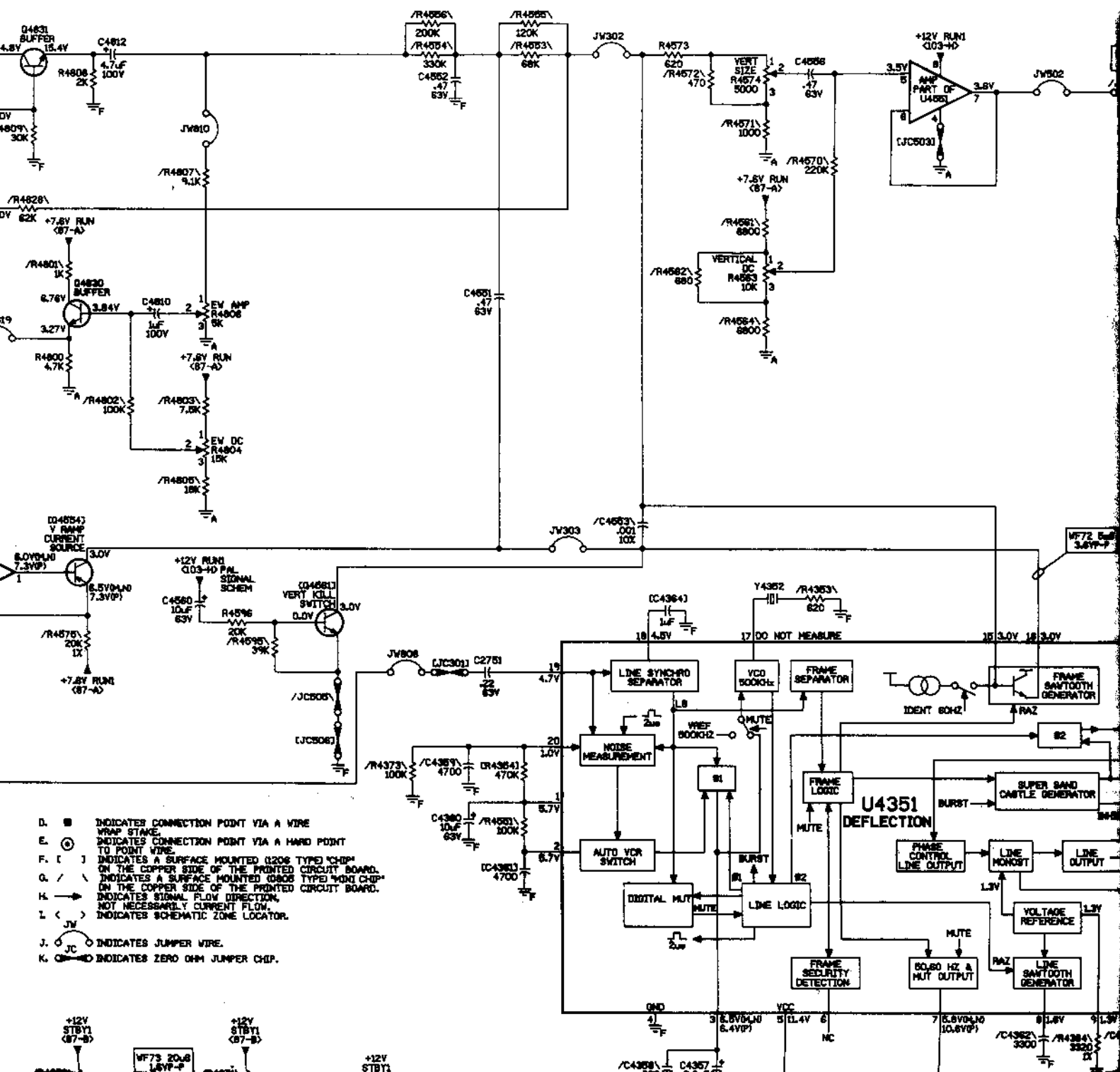


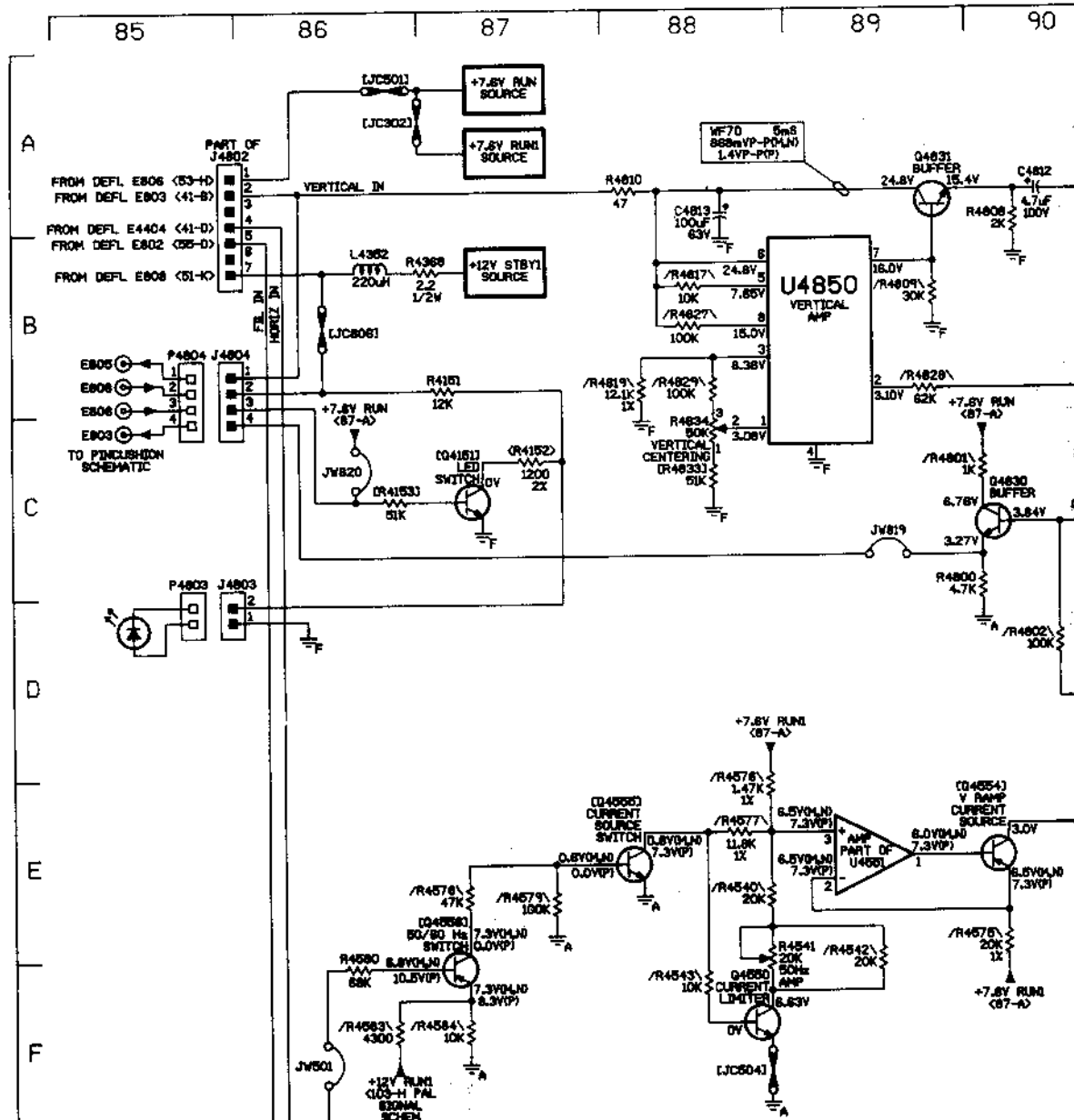
DIAGRAMA ESQUEMÁTICO DO SINAL DE PAL II

97 98 99



90 91 92 93 94 95 96 97

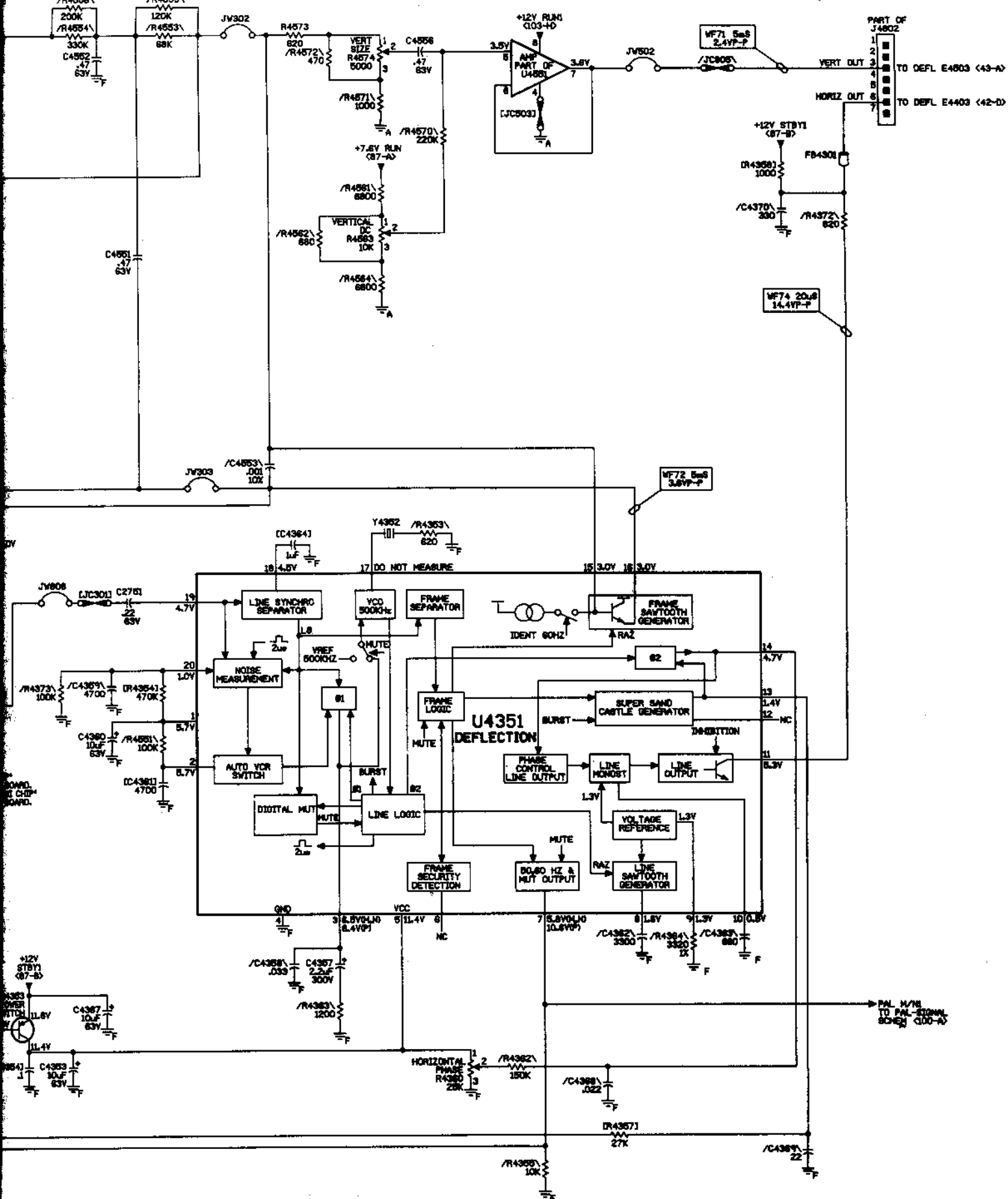




SCHEMATIC NOTES

1. RESISTORS
 - A. SURFACE MOUNTED 1206'S ARE 1/8W, 5% TOLERANCE, 0805'S ARE 1/10W 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
 - B. AXIAL LEADED ARE 1/4W, 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
 - C. VALUES ARE IN OHMS, K=1,000, M=1,000,000.
2. CAPACITORS
 - A. VALUES 1.0 AND ABOVE ARE IN pF, UNLESS OTHERWISE SPECIFIED.
 - B. VALUES BELOW 1.0 ARE IN uF, UNLESS OTHERWISE SPECIFIED.
 - C. FOR TOLERANCE INFORMATION LOOK IN THE PARTS LIST, UNLESS OTHERWISE SPECIFIED.
 - D. ALL ARE 50V, UNLESS OTHERWISE SPECIFIED.
3. SYMBOLS
 - A. TP INDICATES SERVICE TEST POINT.
 - B. X INDICATES COLD GROUND.
 - C. X INDICATES HOT GROUND.

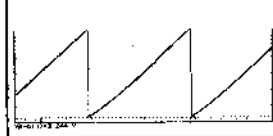
- D. ■ INDICATE WARP ST
- E. ○ INDICATE TO PUMP
- F. [] INDICATE ON THE
- G. / \ INDICATE ON THE
- H. → INDICATE NOT NEI
- I. < > INDICATE
- J. ○ INDICATE
- K. ○ INDICATE



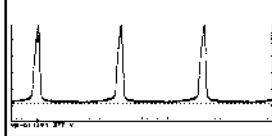
WF72(m,n)



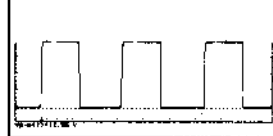
WF72(p)

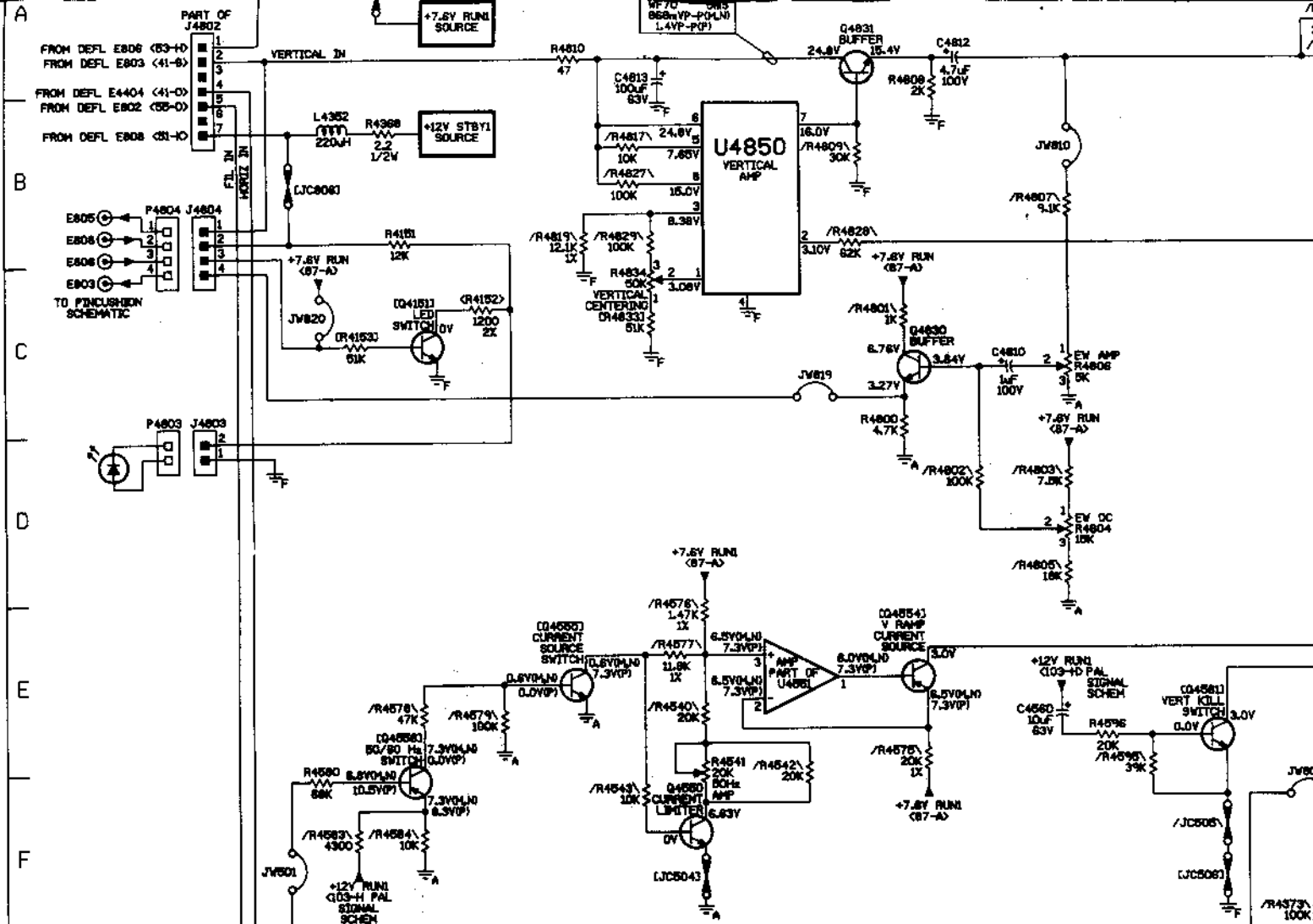


WF73



WF74





SCHEMATIC NOTES

1. RESISTORS

- A. SURFACE MOUNTED 1206'S ARE 1/8W, 5% TOLERANCE, 0805'S ARE 1/10W, 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
- B. AXIAL LEADED ARE 1/4W, 5% TOLERANCE, UNLESS OTHERWISE SPECIFIED.
- C. VALUES ARE IN OHMS, K=X1000, M=X1,000,000.

2. CAPACITORS

- A. VALUES 10 AND ABOVE ARE IN pF, UNLESS OTHERWISE SPECIFIED.
- B. VALUES BELOW 10 ARE IN uF, UNLESS OTHERWISE SPECIFIED.
- C. FOR TOLERANCE INFORMATION LOOK IN THE PARTS LIST, UNLESS OTHERWISE SPECIFIED.
- D. ALL ARE 50V, UNLESS OTHERWISE SPECIFIED.

3. SYMBOLS

- A. TP INDICATES SERVICE TEST POINT.
- B. X INDICATES COLD GROUND.
- C. X INDICATES HOT GROUND.

D. ■ INDICATES CONNECTION POINT VIA A WIRE WRAP STAKE.

E. ○ INDICATES CONNECTION POINT VIA A HARD POINT TO POINT WIRE.

F. [] INDICATES A SURFACE MOUNTED (1206 TYPE) CHIP ON THE COPPER SIDE OF THE PRINTED CIRCUIT BOARD.

G. / \ INDICATES A SURFACE MOUNTED (0805 TYPE) CHIP ON THE COPPER SIDE OF THE PRINTED CIRCUIT BOARD.

H. → INDICATES SIGNAL FLOW DIRECTION, NOT NECESSARILY CURRENT FLOW.

I. < > INDICATES SCHEMATIC ZONE LOCATOR.

J. JVC INDICATES JUMPER WIRE.

K. 0 INDICATES ZERO OHM JUMPER CHIP.

DC VOLTAGE AND WAVEFORM NOTES

- 00 = PAL M VOLTAGE
- 00 = NTSC VOLTAGE
- 01 = PAL N VOLTAGE
- NO LETTER = ALL SIGNALS

WAVEFORMS

m=PAL M waveform n=NTSC waveform p=PAL N waveform no letter = all signals

